

Demystifying and Improving Lazy Promotion in Cache Eviction



Qinghan Chen¹



Muhammad Haekal
Muhyidin Al-Araby²



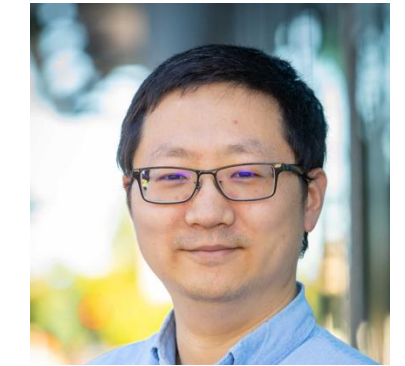
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Presenter



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Juncheng Yang³



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Parallel Data Laboratory



² Sepuluh Nopember Institute of Technology



³ Harvard University

LRU promotions limit scalability

Request → Cache lookup

cached objects



LRU promotions limit scalability

Request → Cache lookup  Hit

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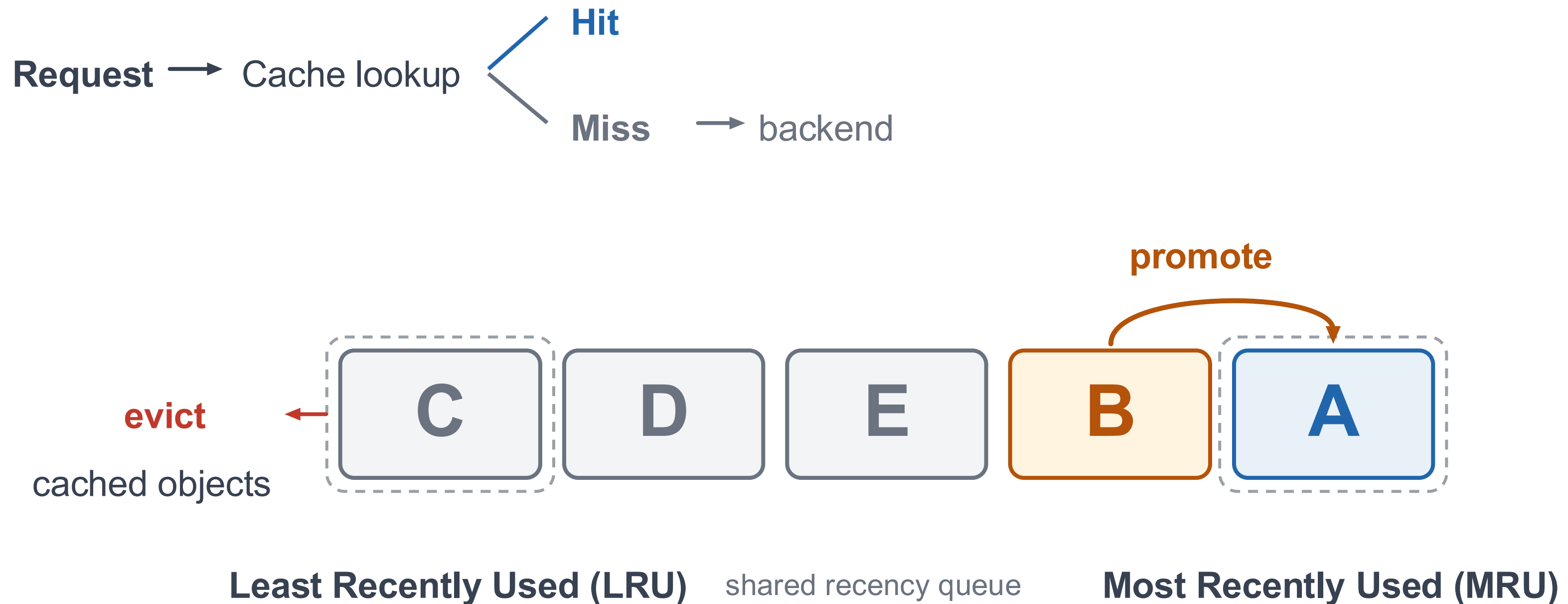


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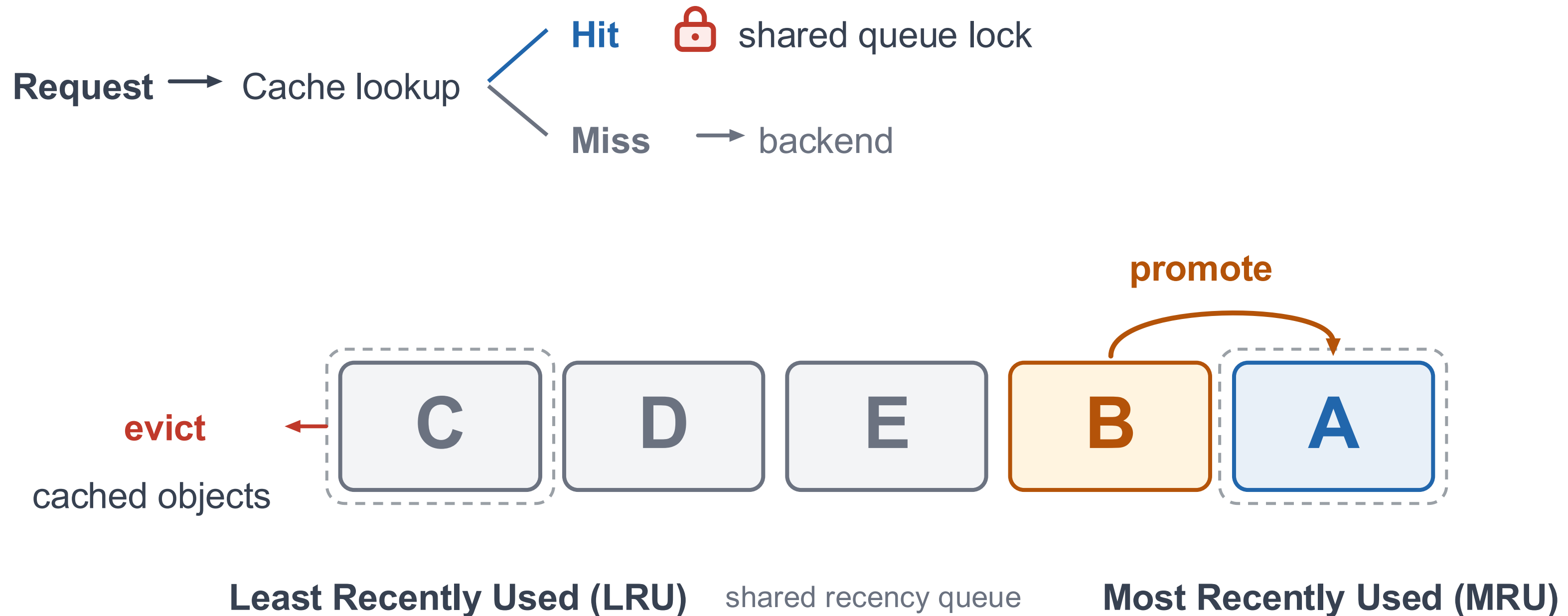
LRU promotions limit scalability

- On a hit, LRU moves the object to the Most Recently Used (MRU) slot.



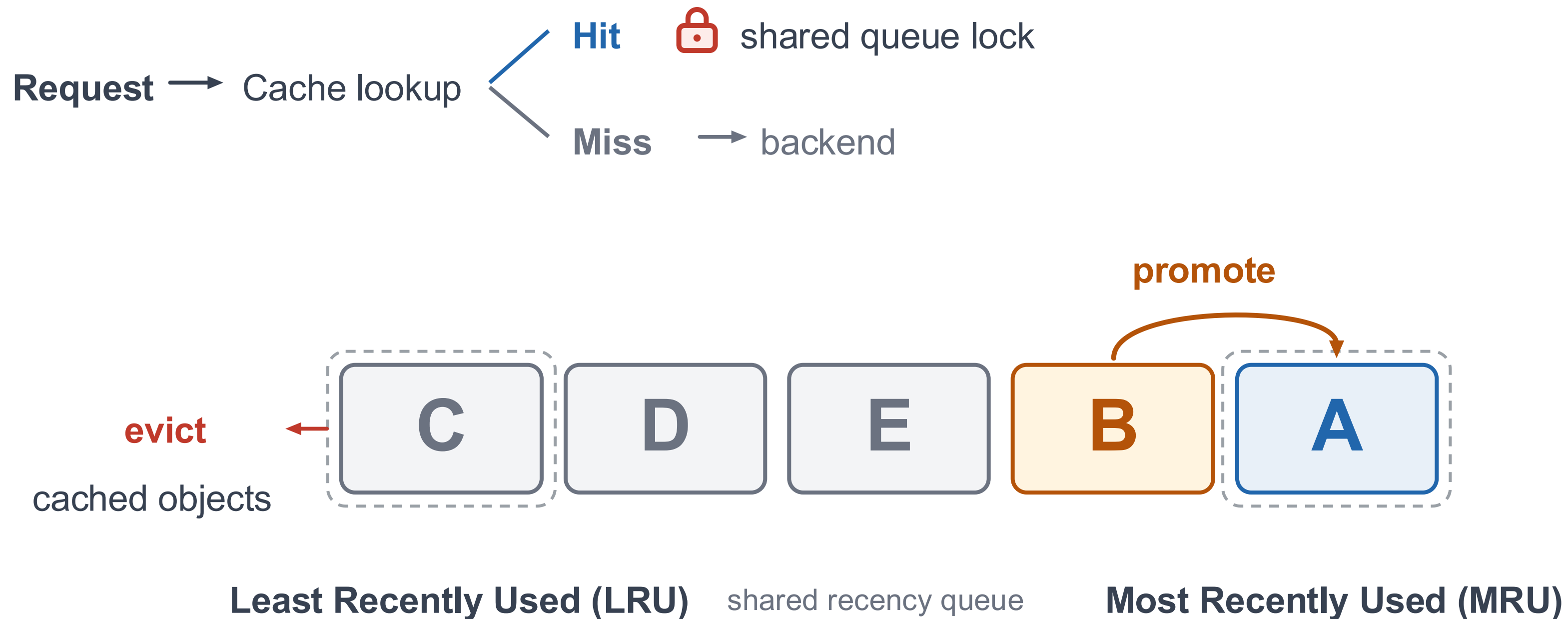
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LRU promotions limit scalability

- On a hit, LRU moves the object to the Most Recently Used (MRU) slot.
- Lazy promotion addresses this bottleneck by intentionally performing fewer promotions.

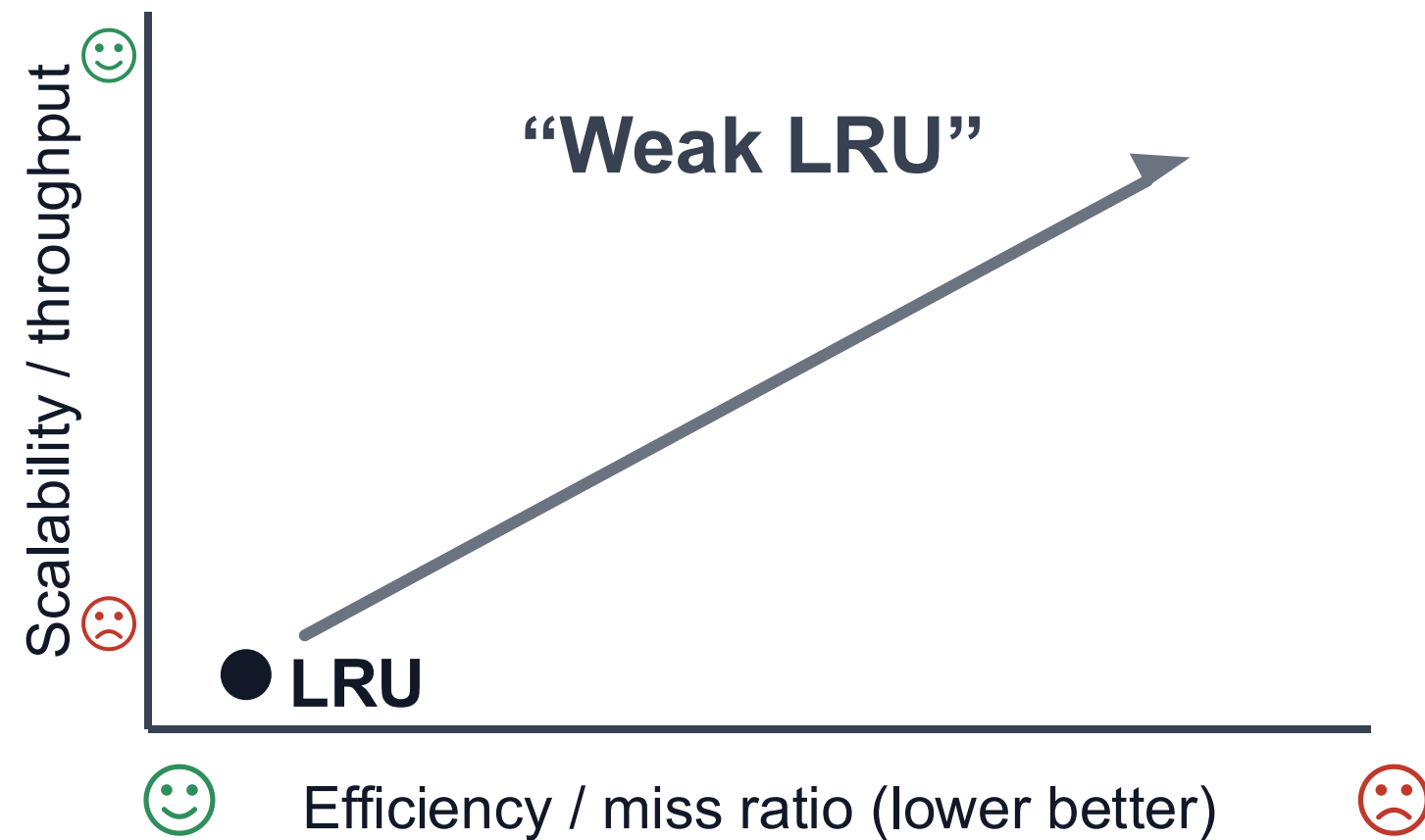


Overview: Lazy promotion = “Weak LRU”?

Conventional wisdom: fewer promotions trade miss ratio for throughput.

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Production systems use five lazy promotion techniques, but no study compares them comprehensively.



HHVM, CacheLib



CliqueMap



Ristretto



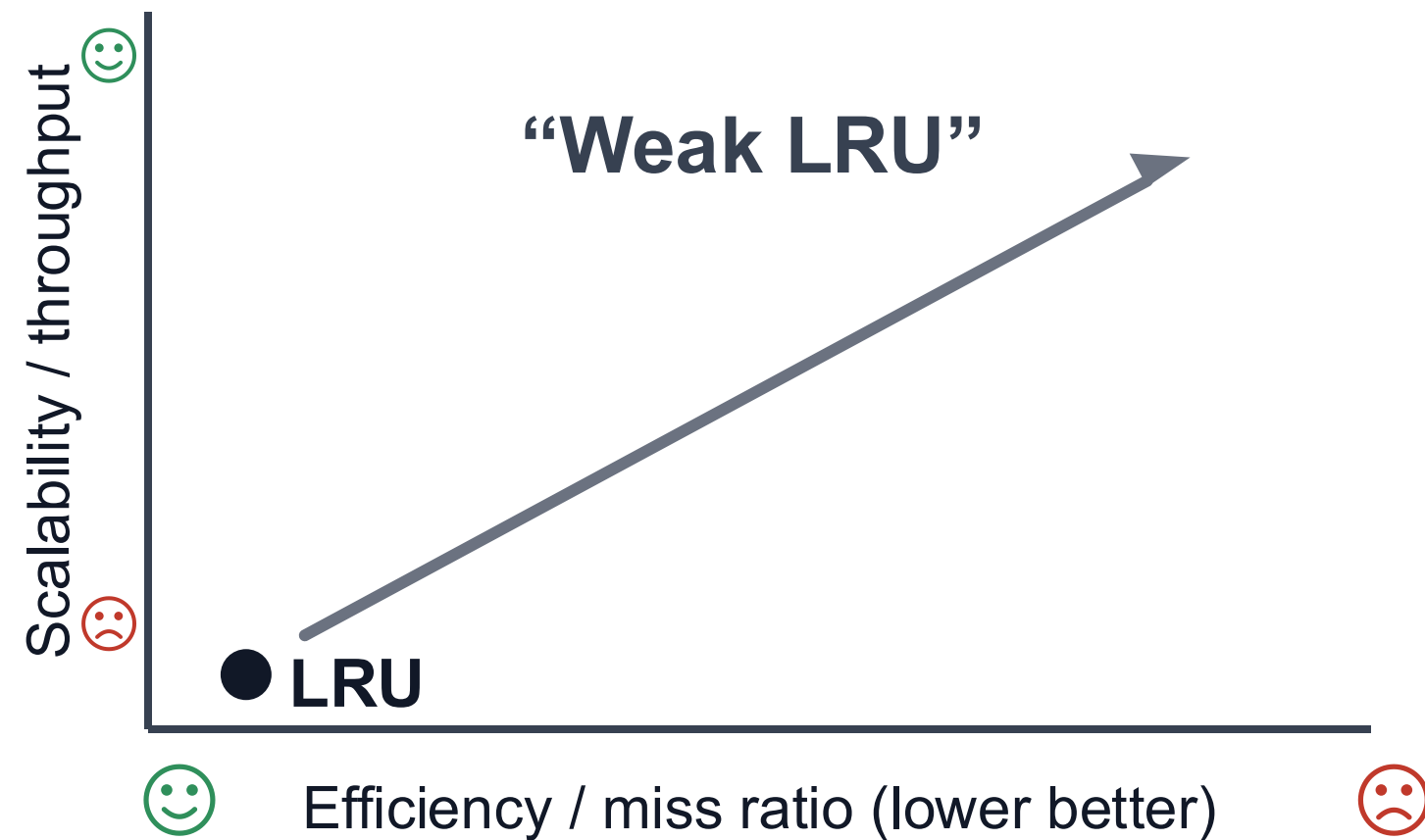
Redis



RocksDB



PostgreSQL



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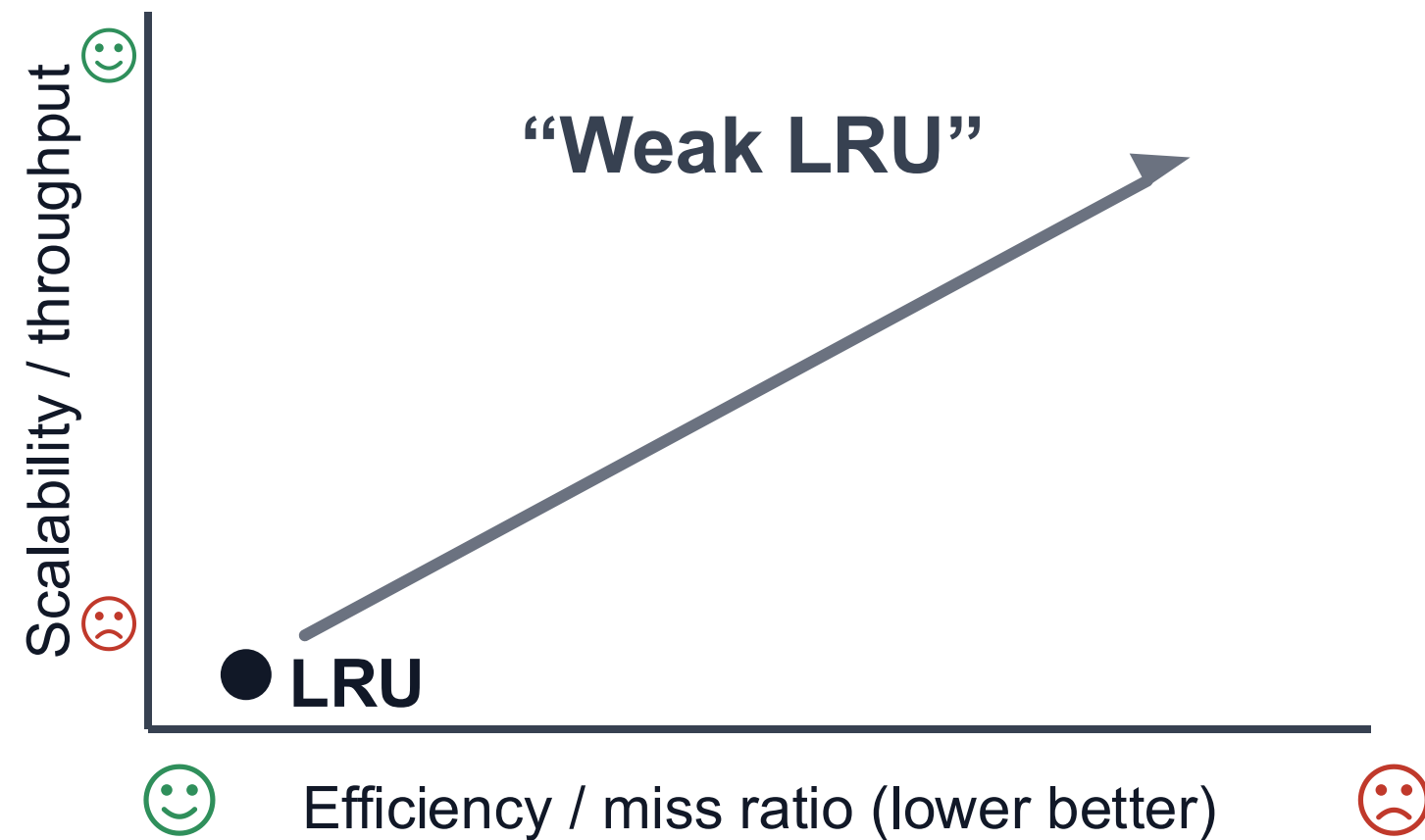
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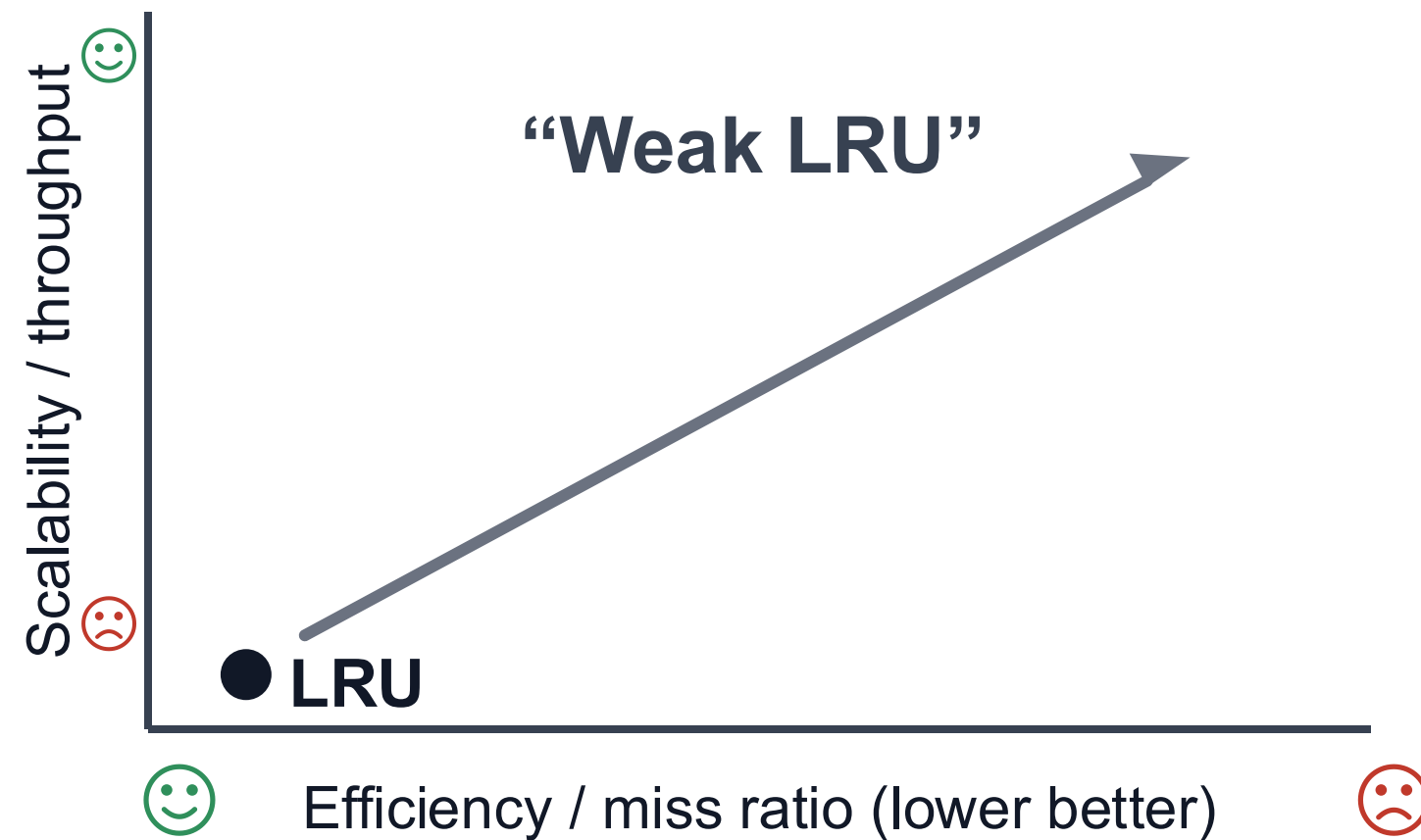
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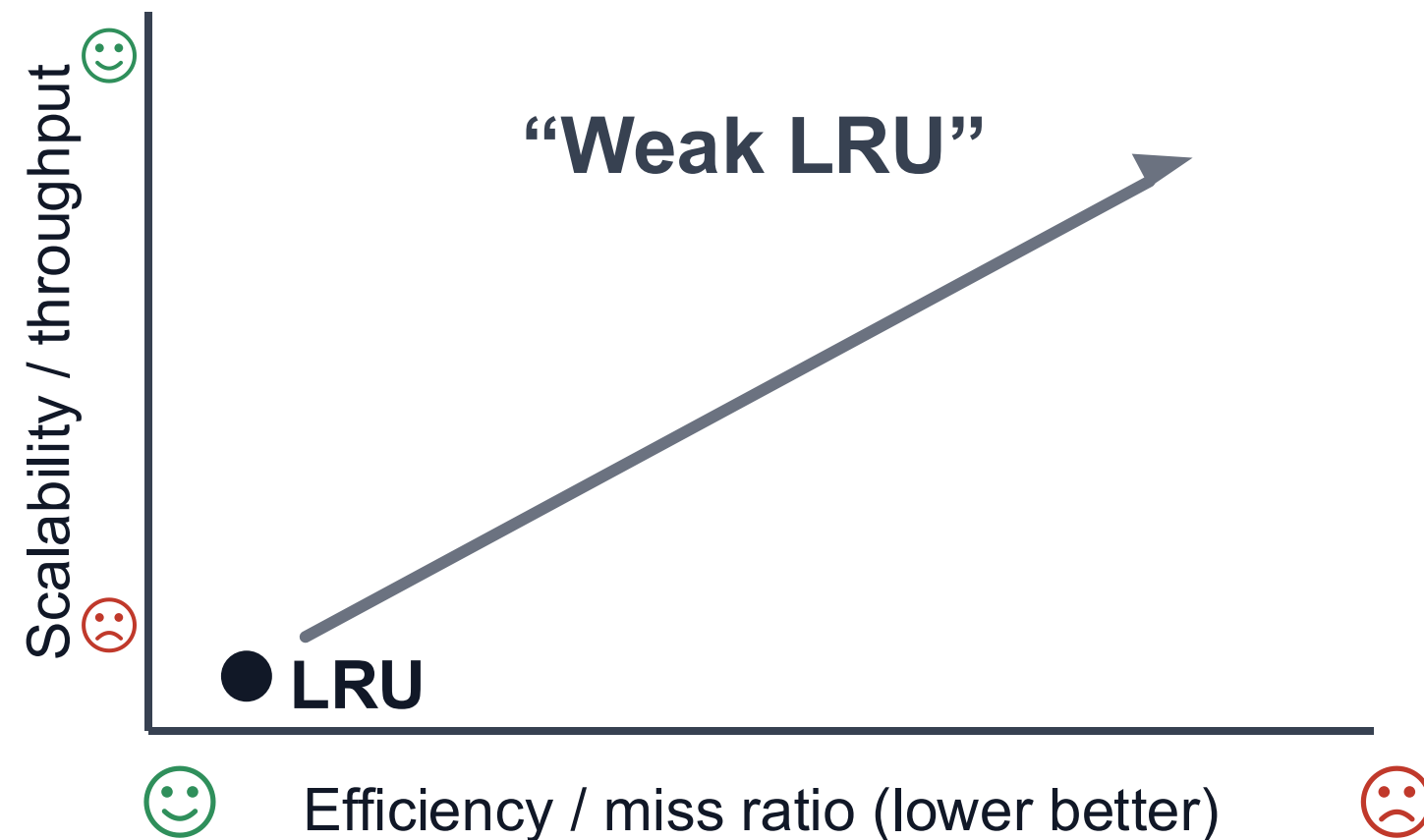
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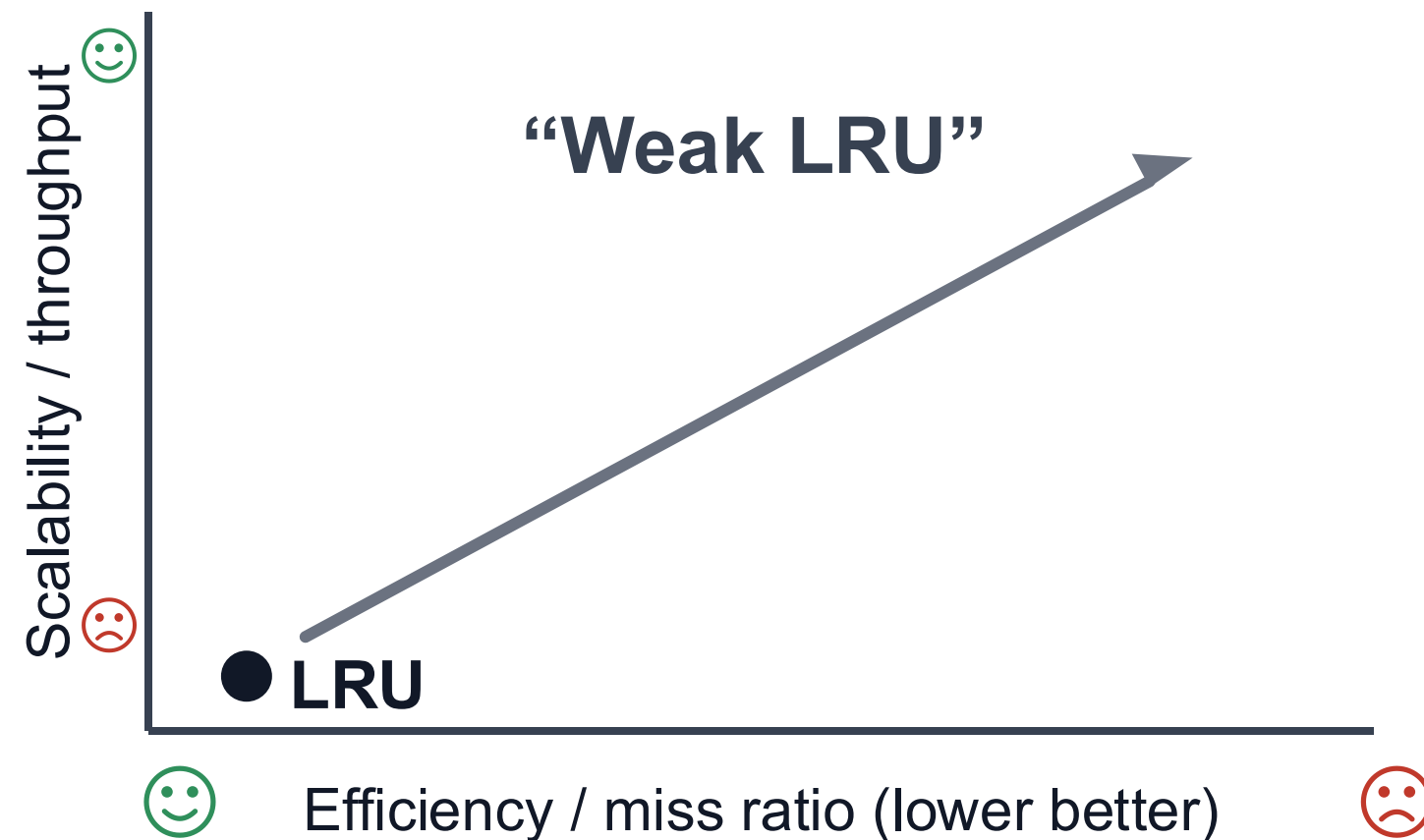
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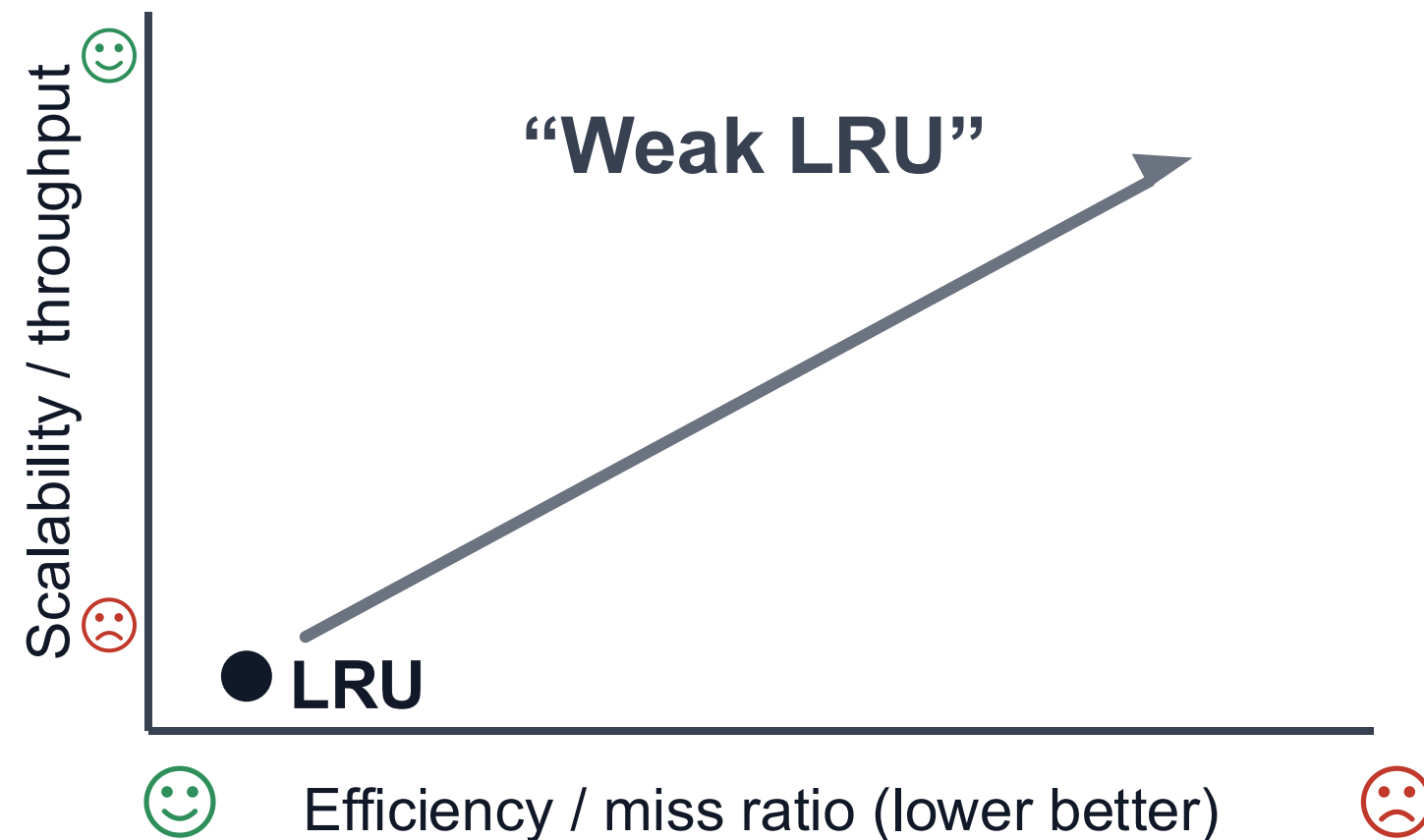
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No. Some techniques improve both axes.
- 2 How effective is each technique?
Very differently. Each has its own trade-off.
- 3 Why do they differ, and can we use that to do better?

First comprehensive evaluation: 5 deployed techniques, 6,357 traces, 346B requests.

Probabilistic-LRU skips promotions, never waits

∞ HHVM

Requests
Example

A #1

B #2

A #3

A #4

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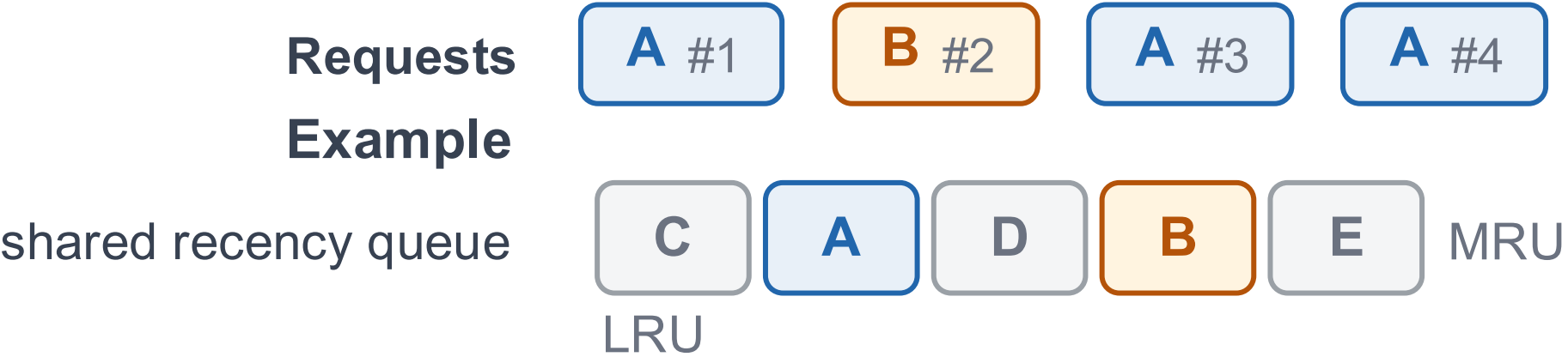
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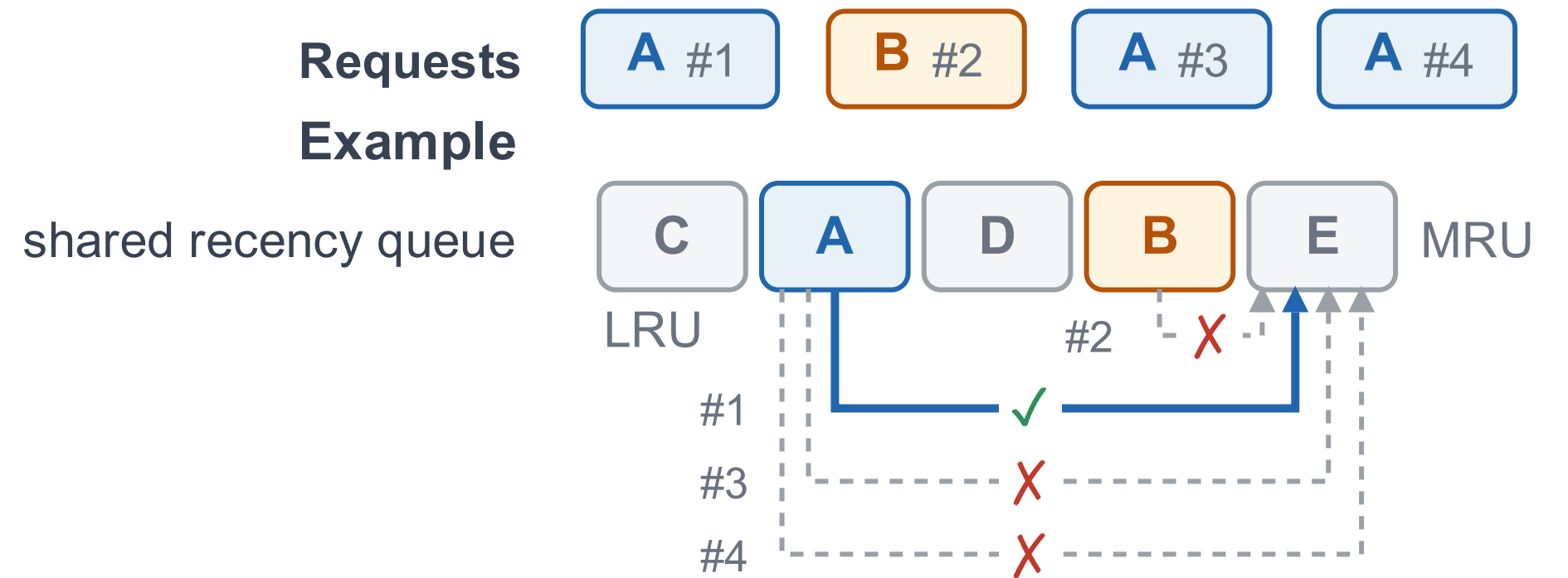


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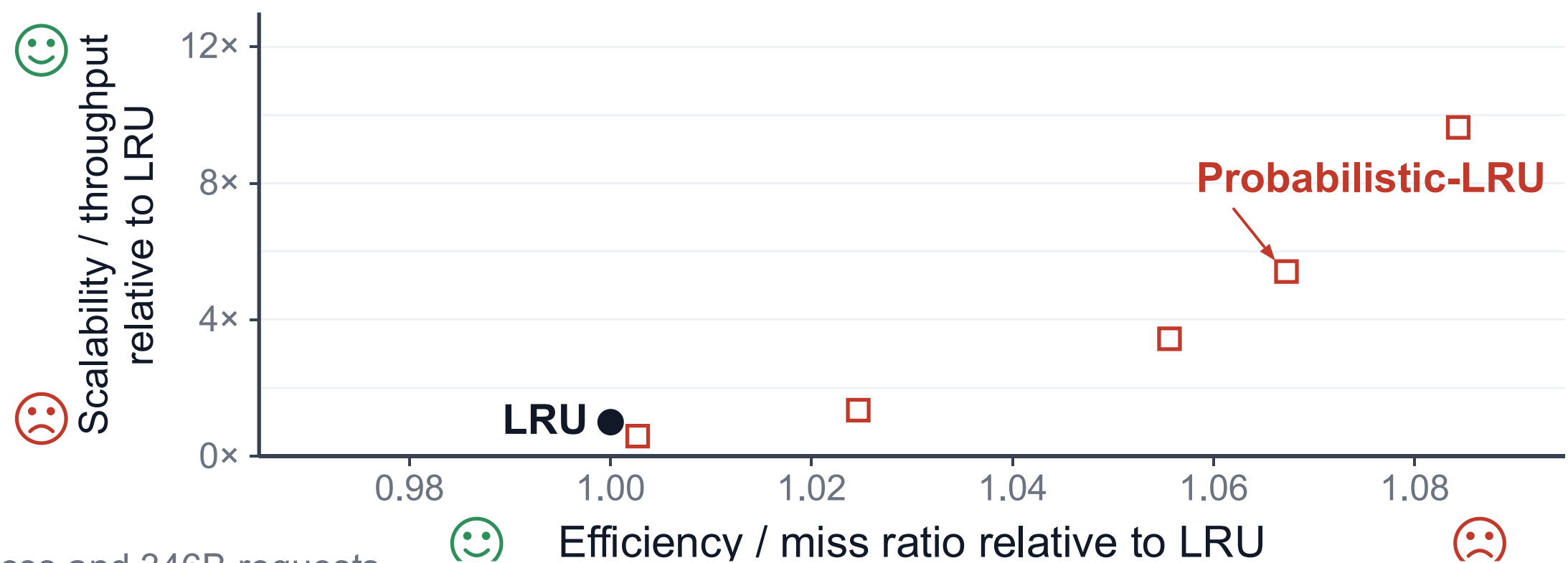
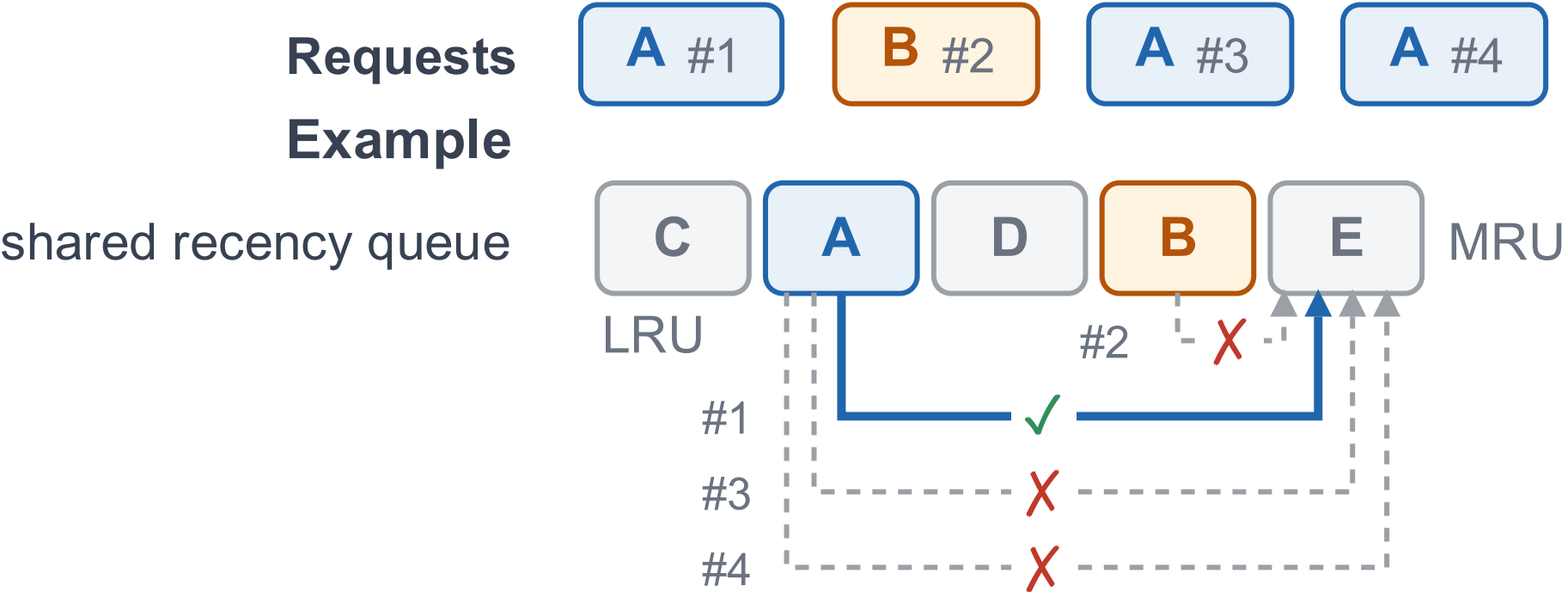
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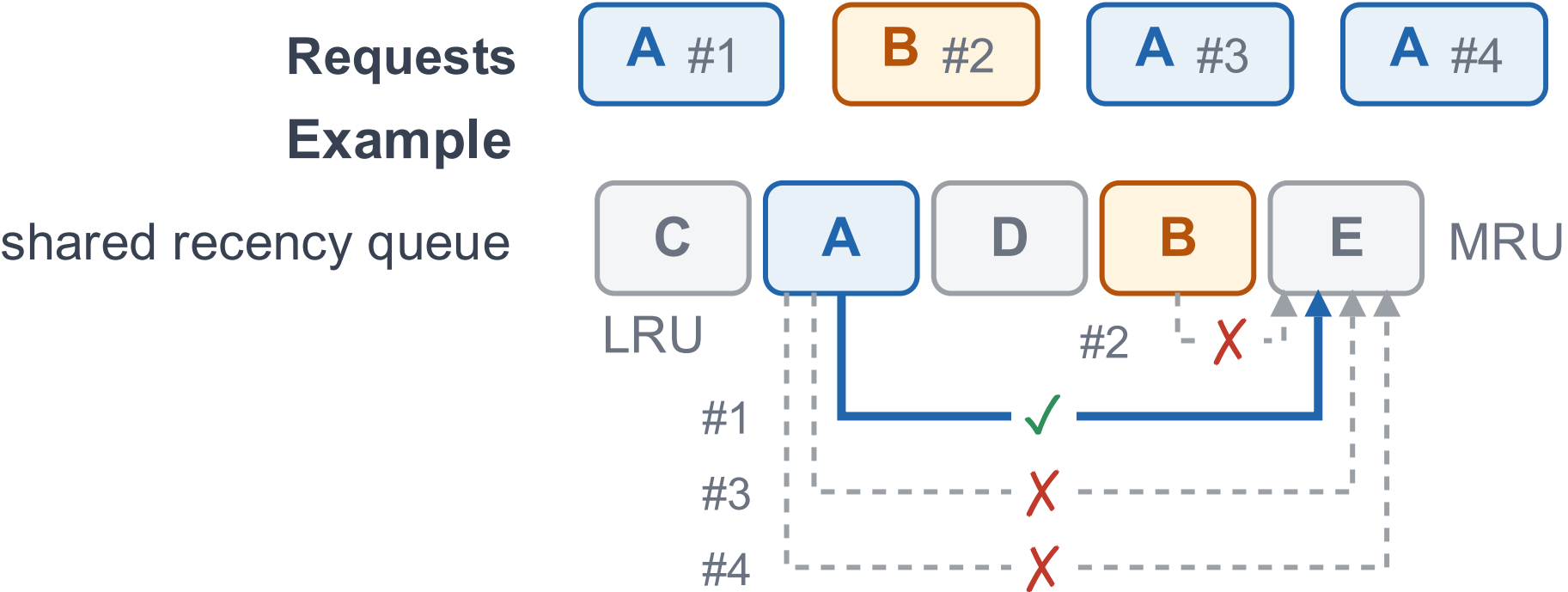
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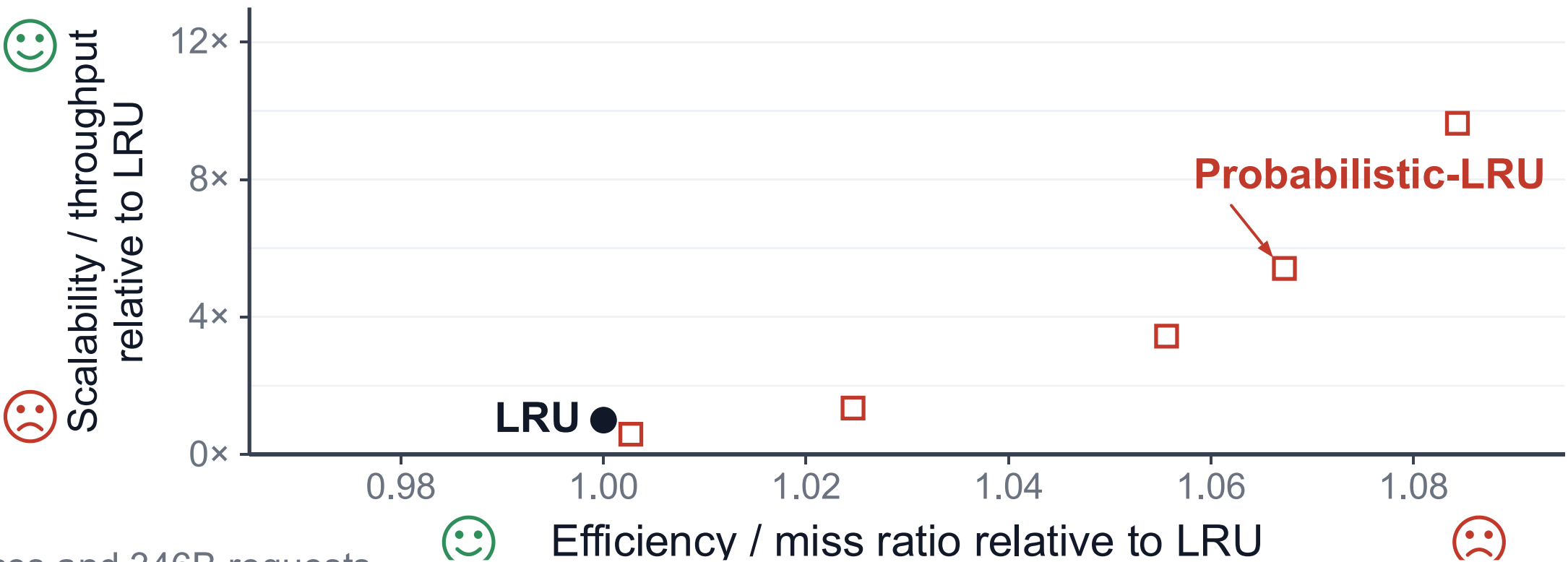
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Takeaway: Try-lock can skip useful promotions, so miss ratio rises quickly.



Batch-LRU amortizes one lock across many hits

 CliqueMap  Ristretto

Requests
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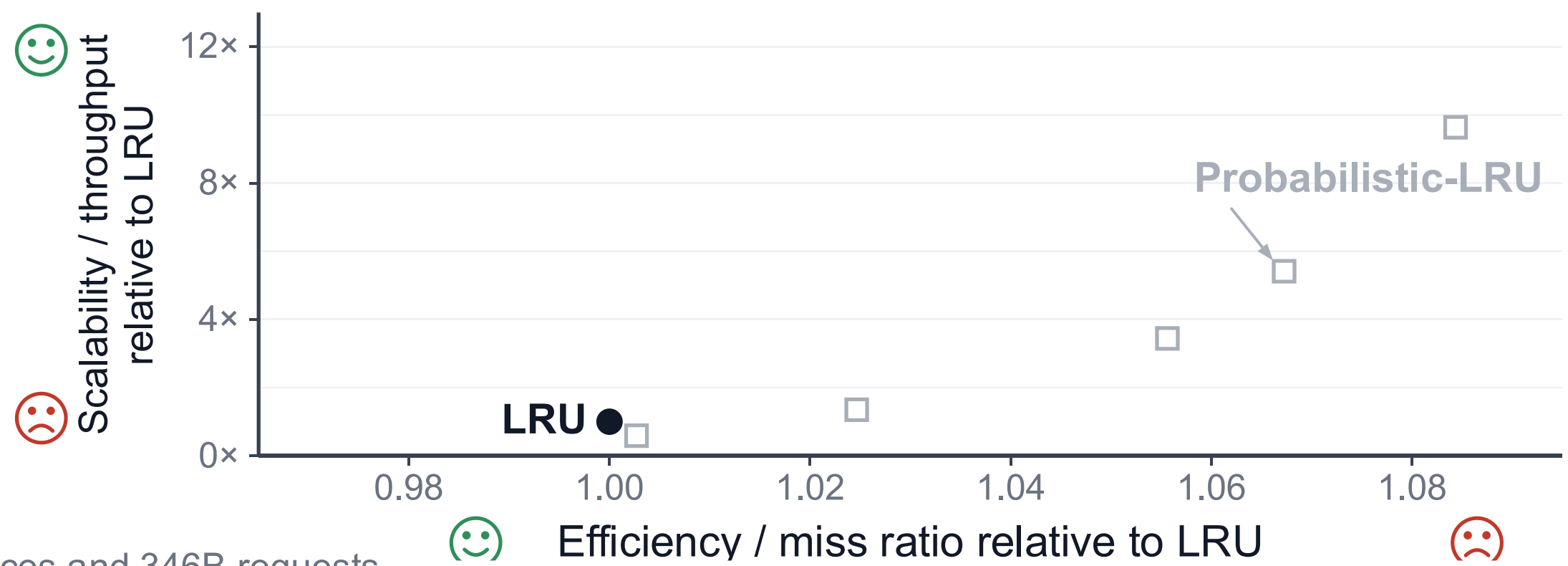
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Core idea: Process many promotions under *one lock*.



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Requests
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Rule: Promote each object *at most once per batch*.

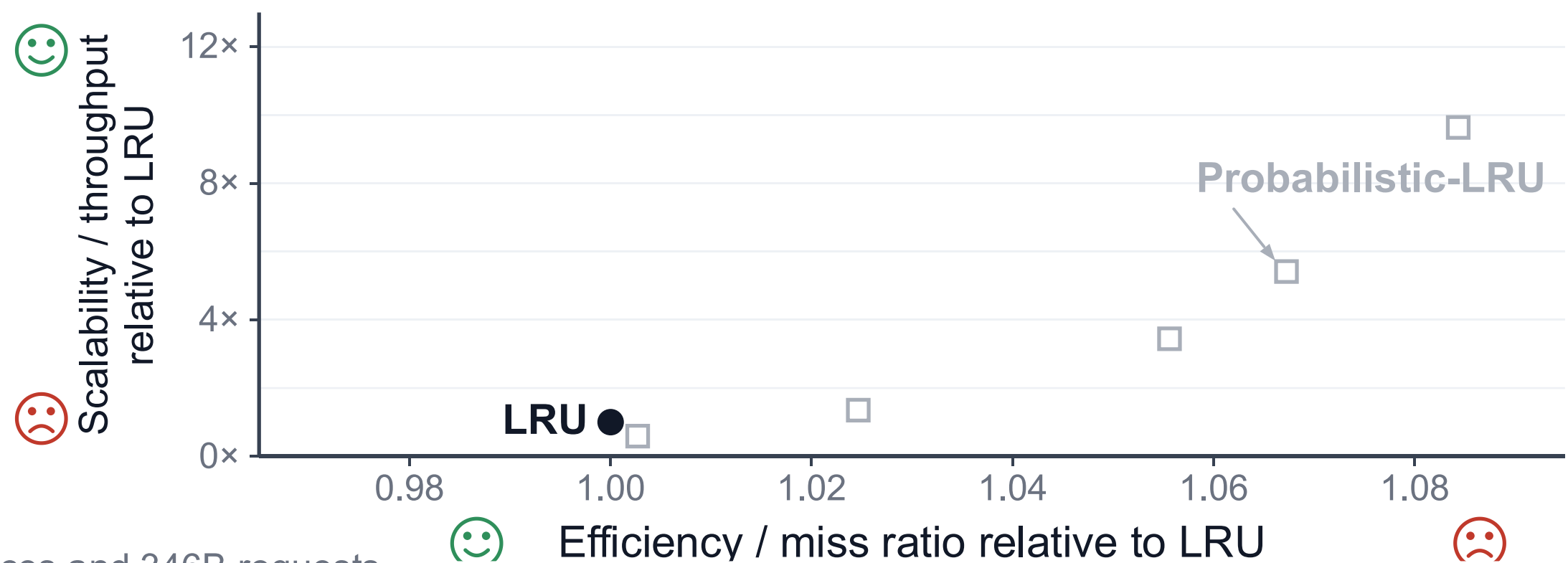
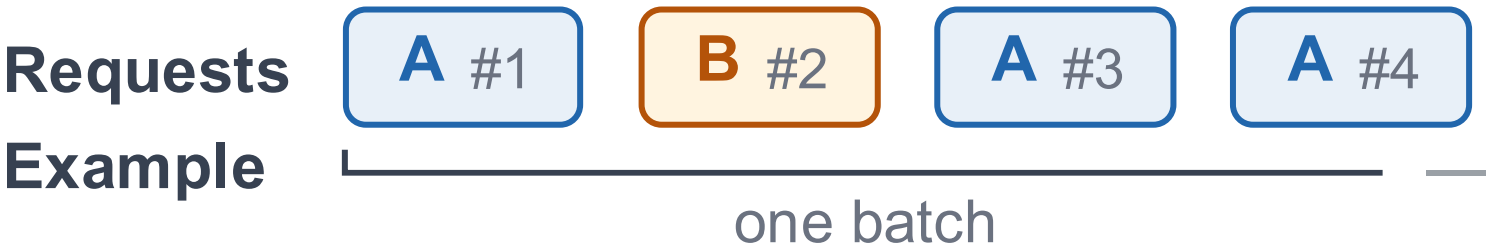


Evaluated on 6,357 traces and 346B requests

Batch-LRU amortizes one lock across many hits

 CliqueMap  Ristretto

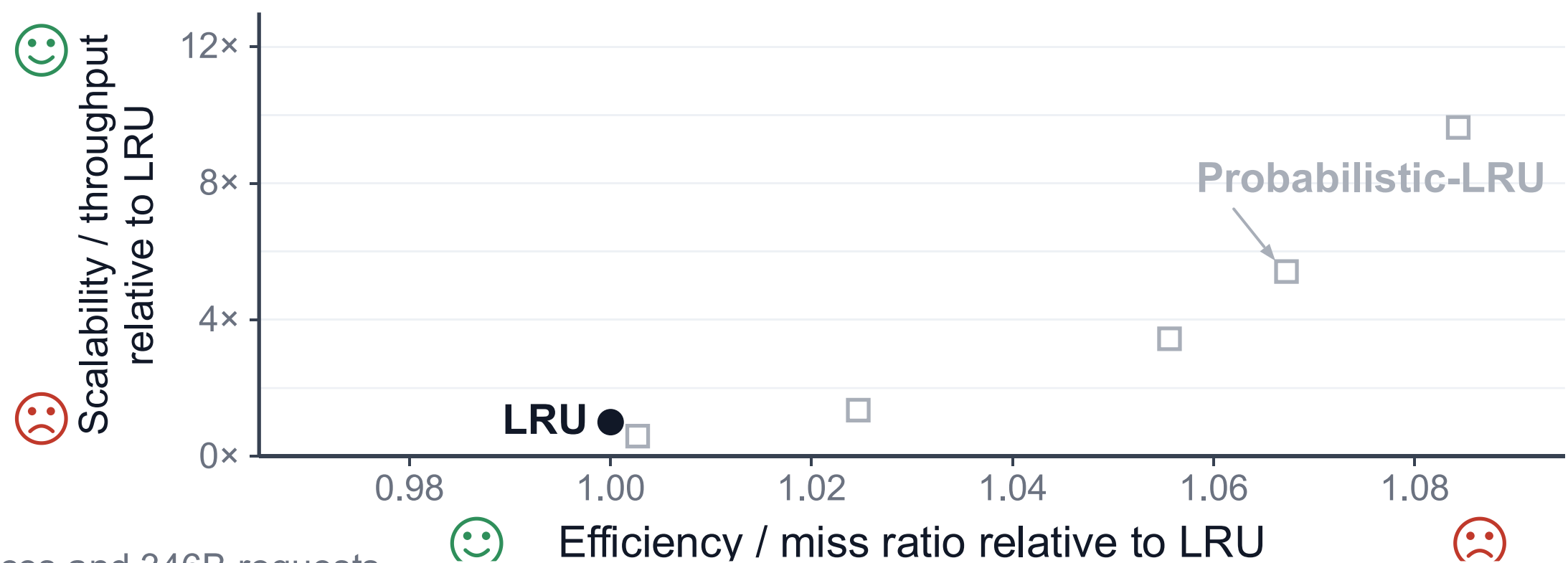
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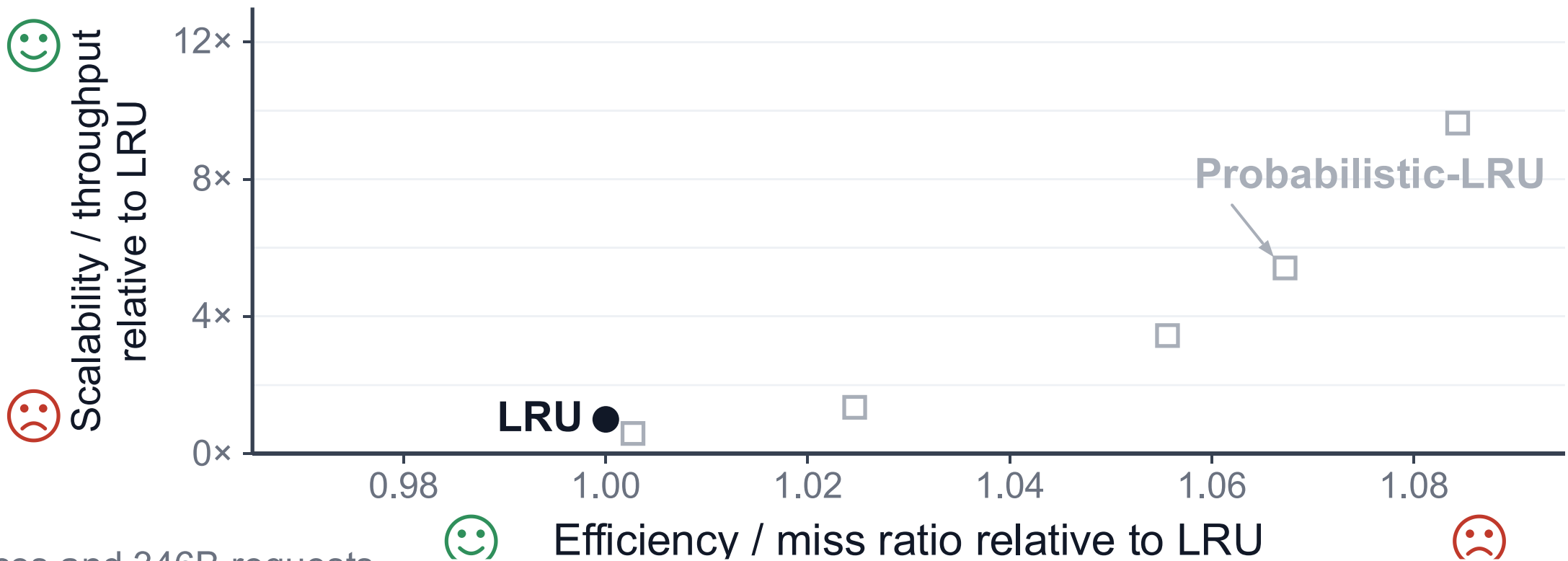
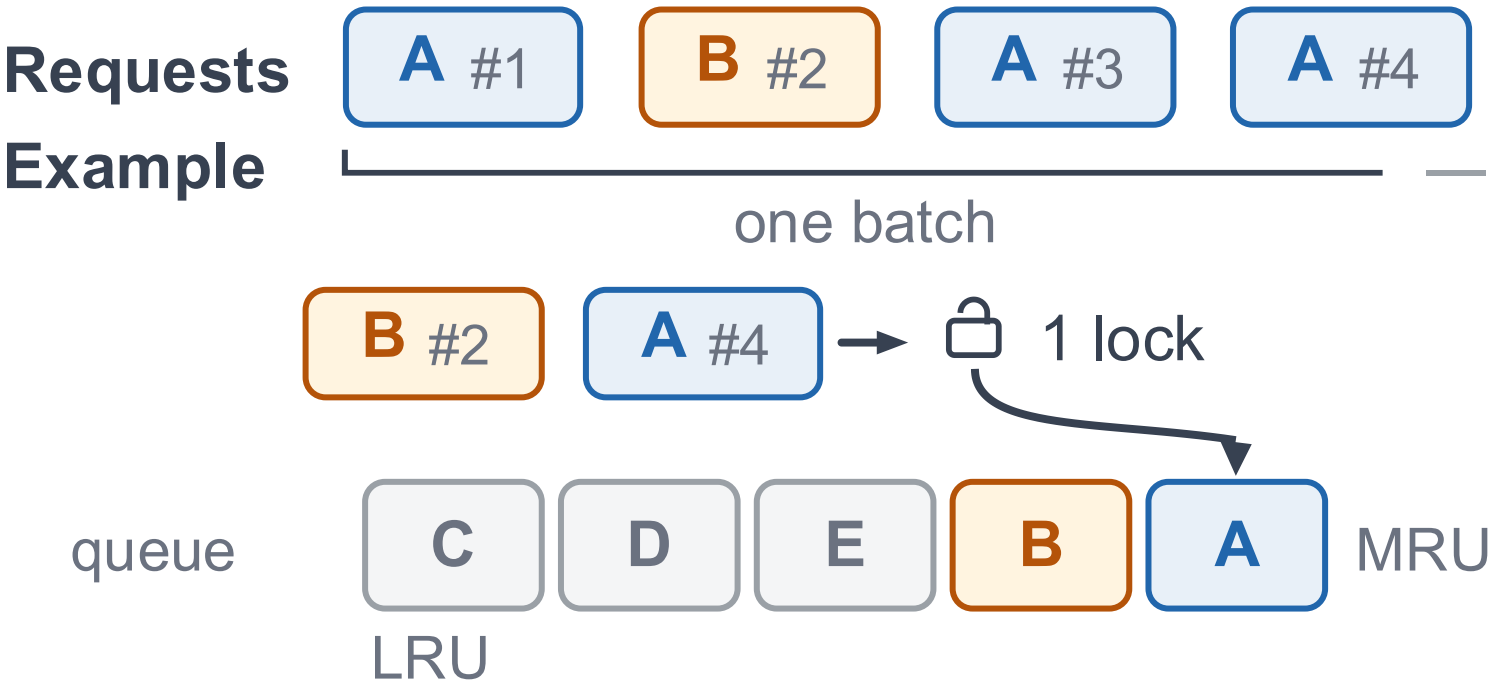
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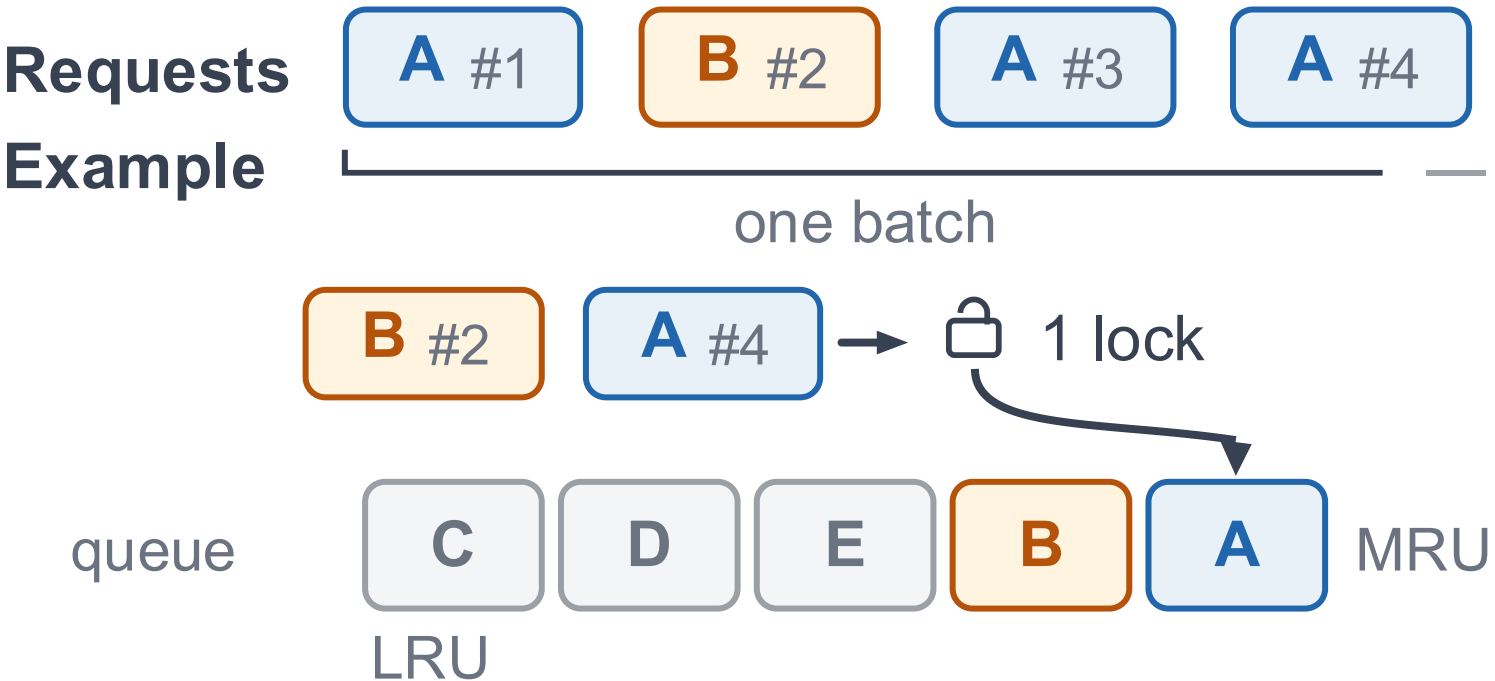
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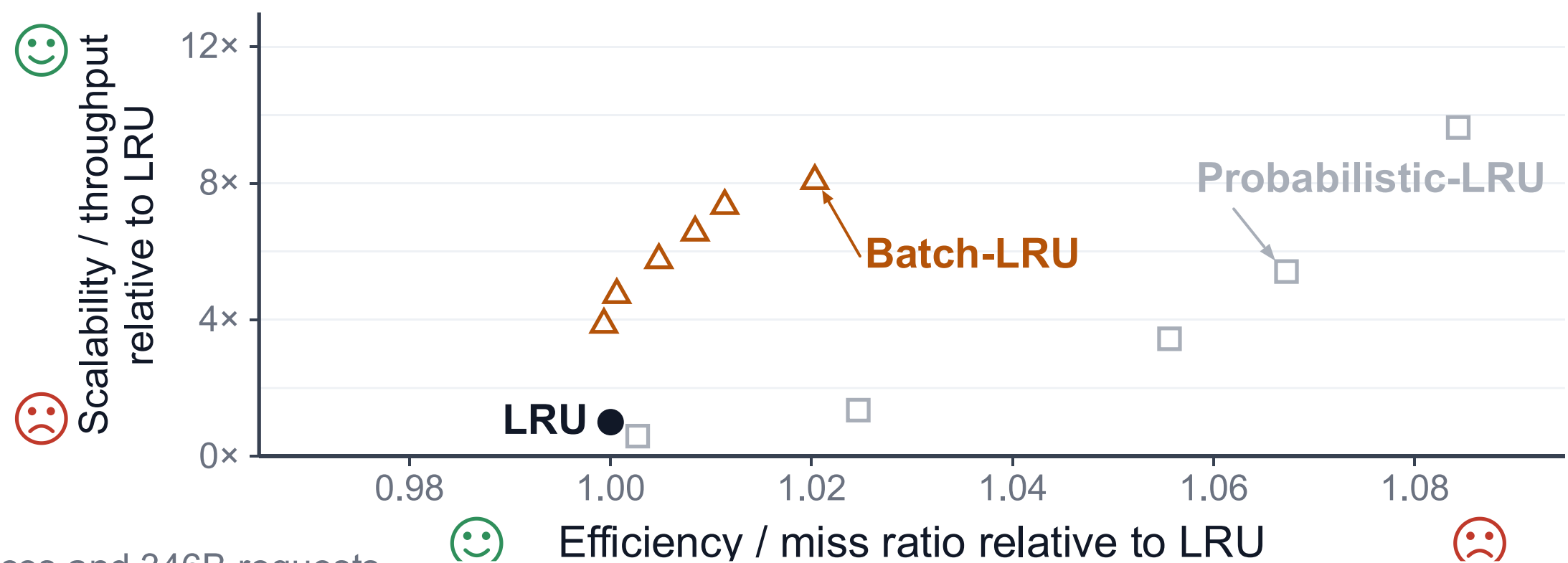
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Takeaway: Batching creates opportunities to eliminate duplicate promotions.



Random-LRU approximates LRU without a queue



Requests

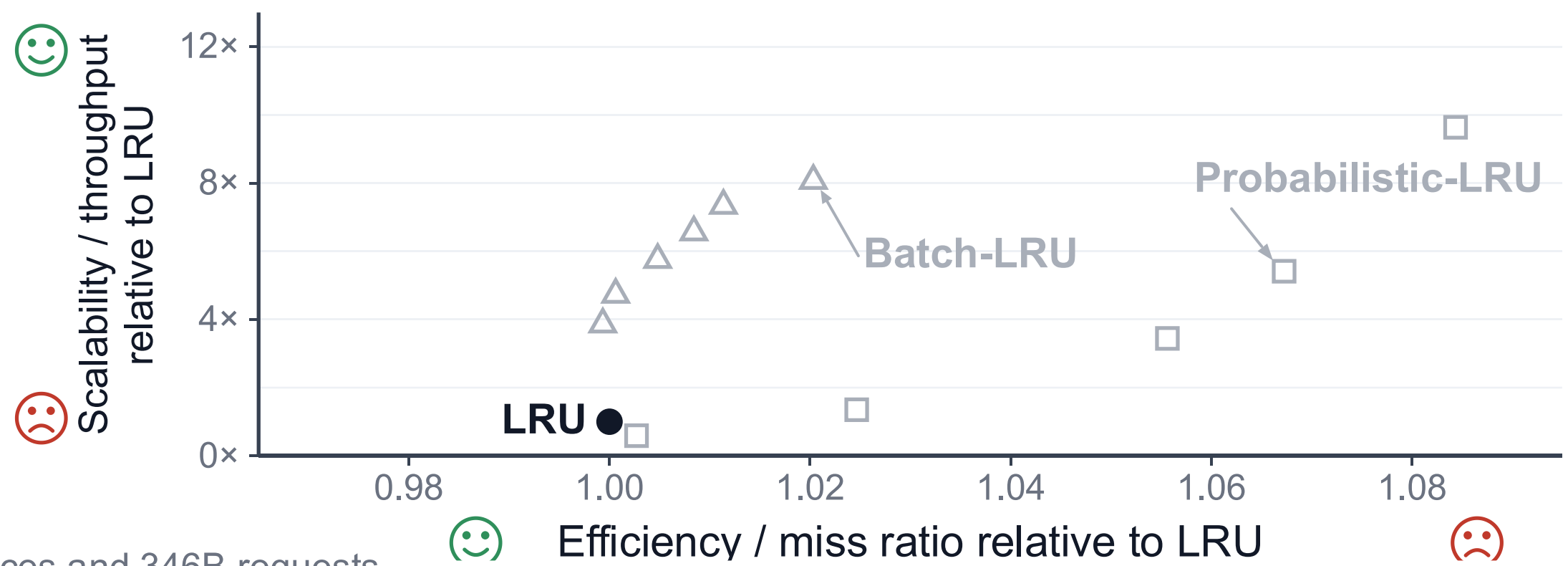
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Requests

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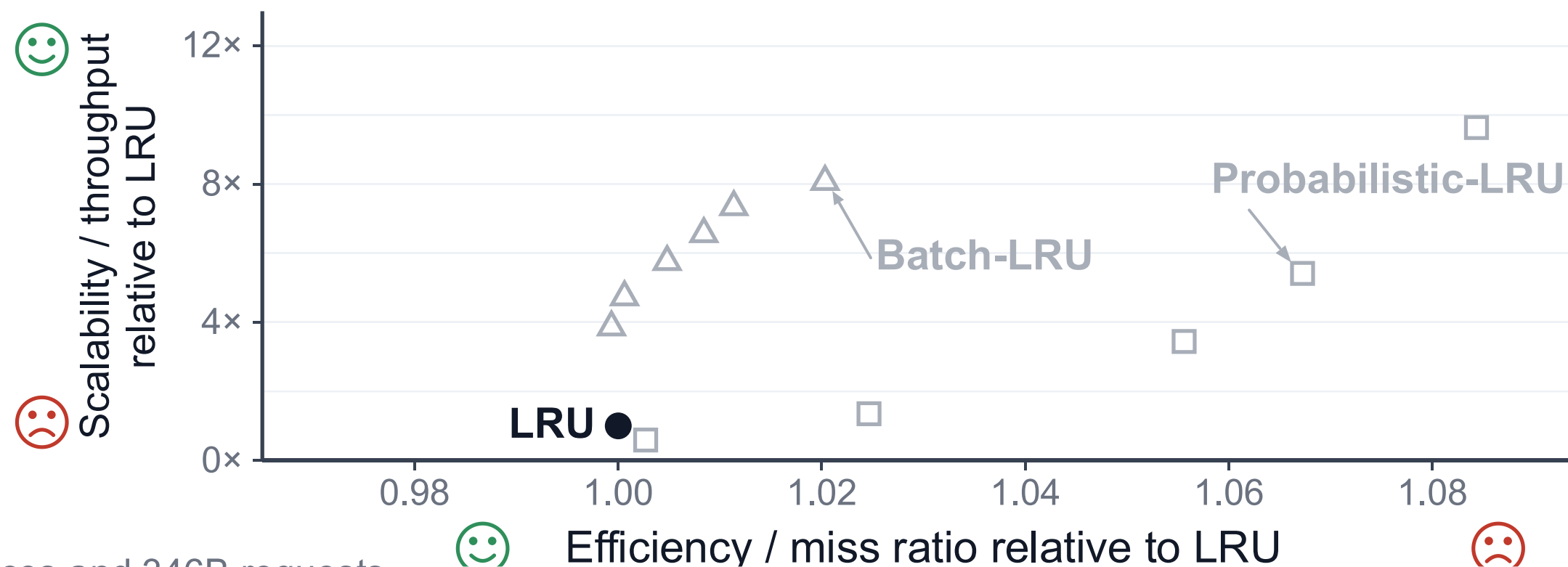
B #2

A #3

A #4

Example

Core idea: Approximate LRU *without a shared recency queue*.



Random-LRU approximates LRU without a queue



Requests

A #1

B #2

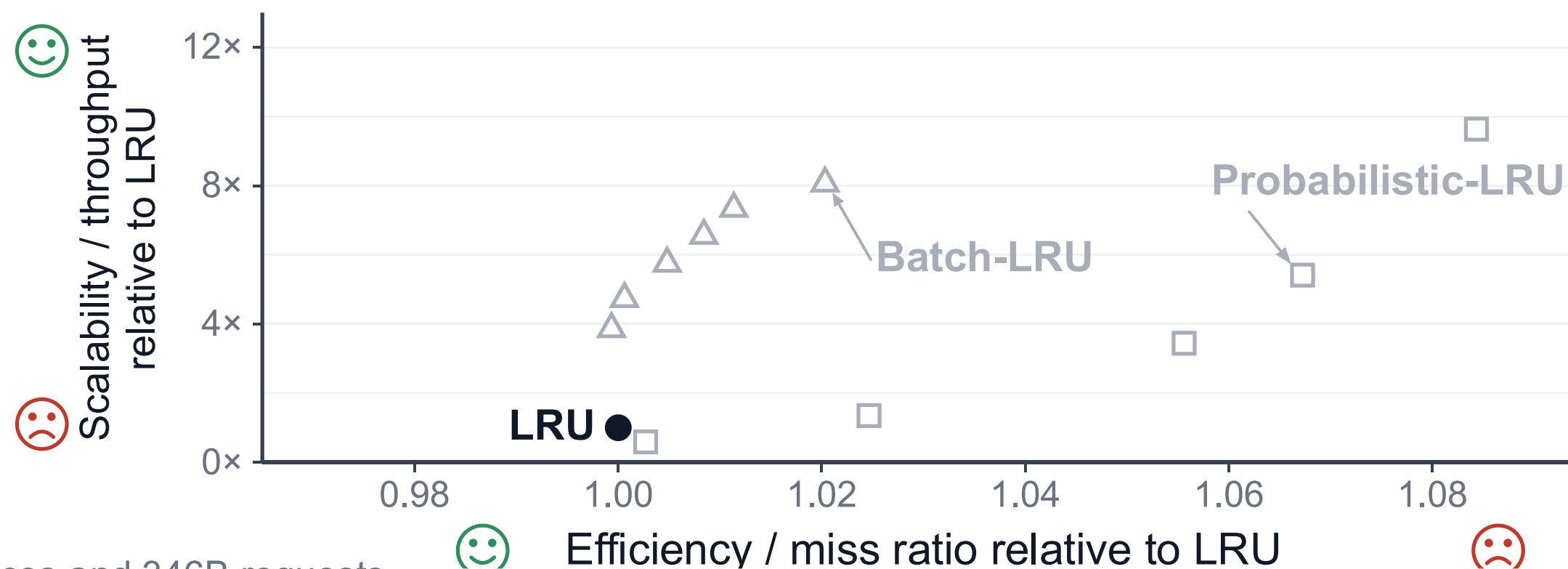
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Example

Core idea: Approximate LRU *without a shared recency queue*.

Rule: Store last-access *timestamps*; evict the *oldest sampled object*.

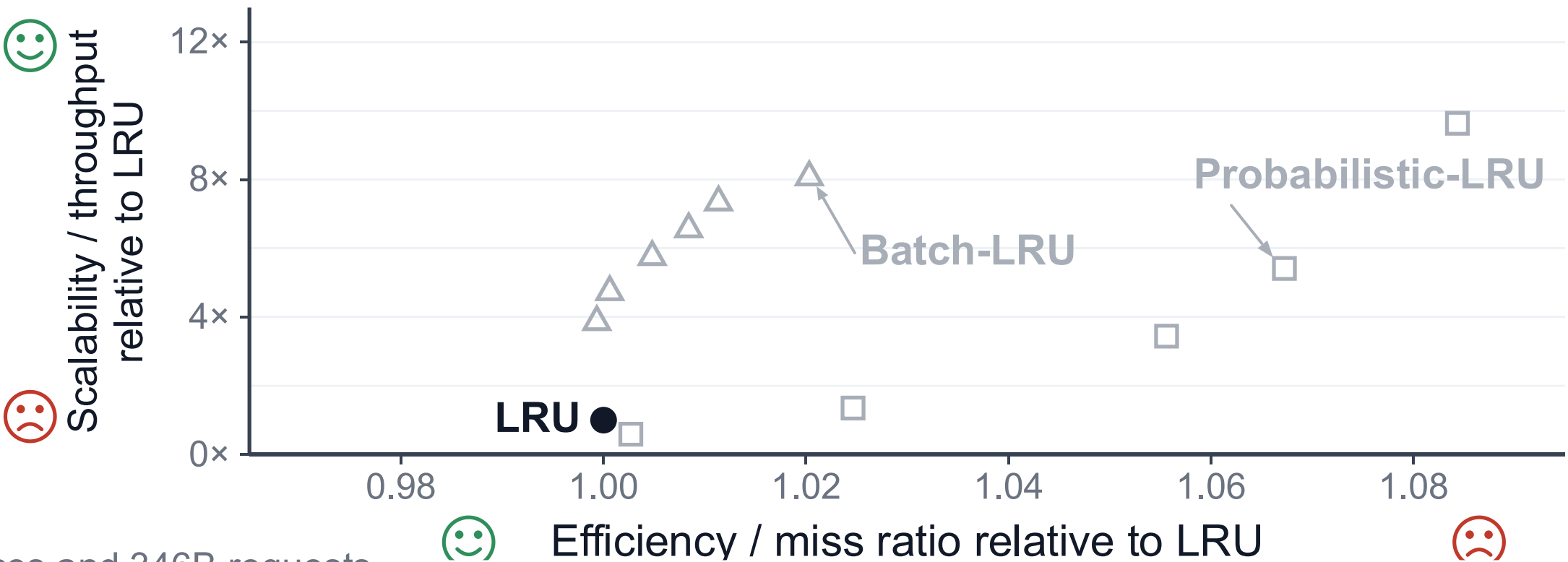


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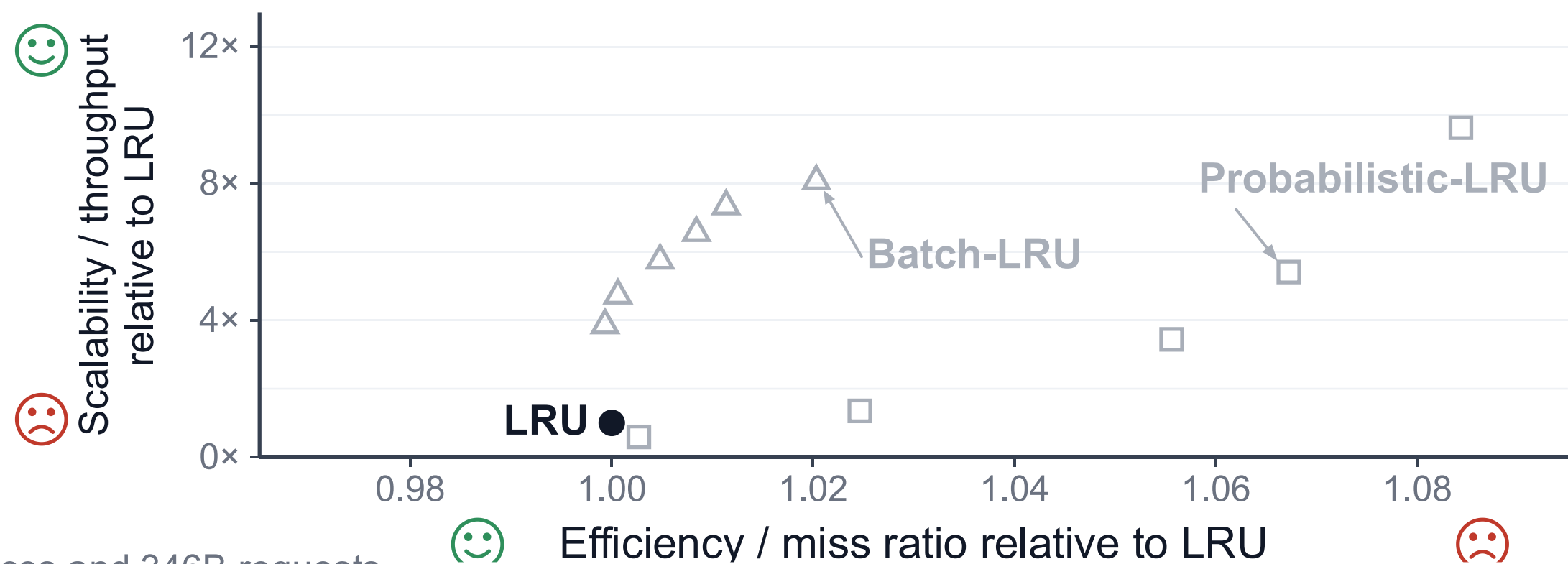
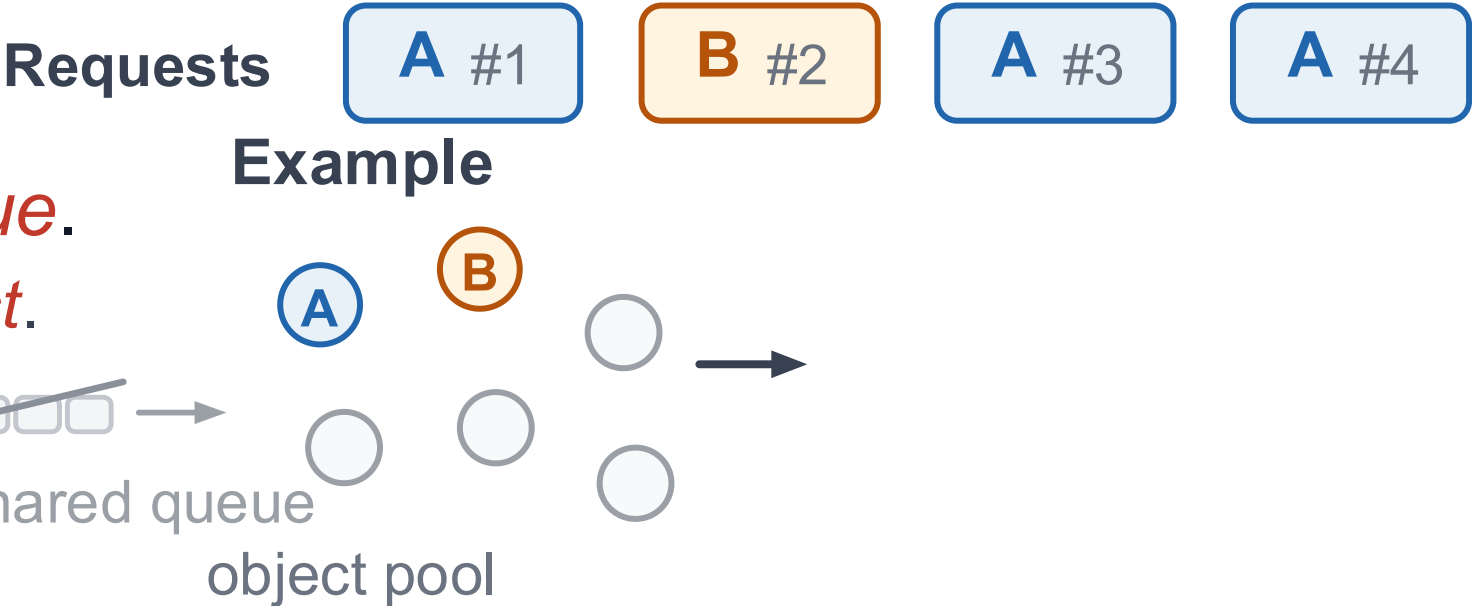
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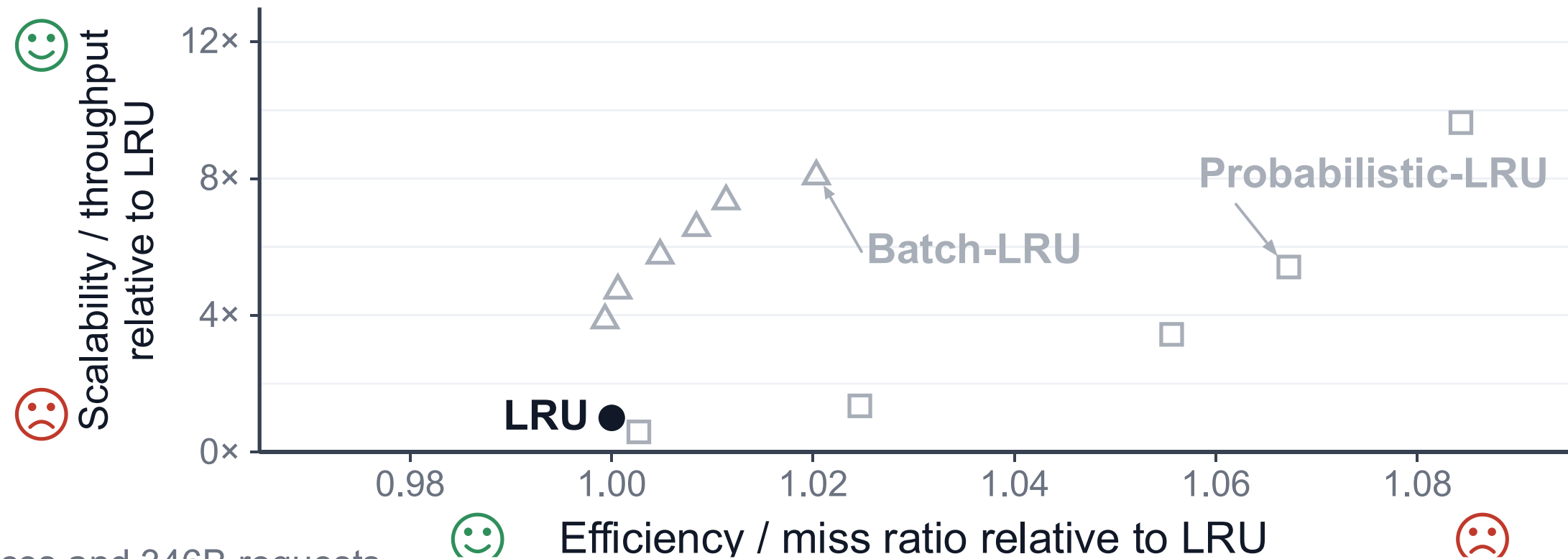
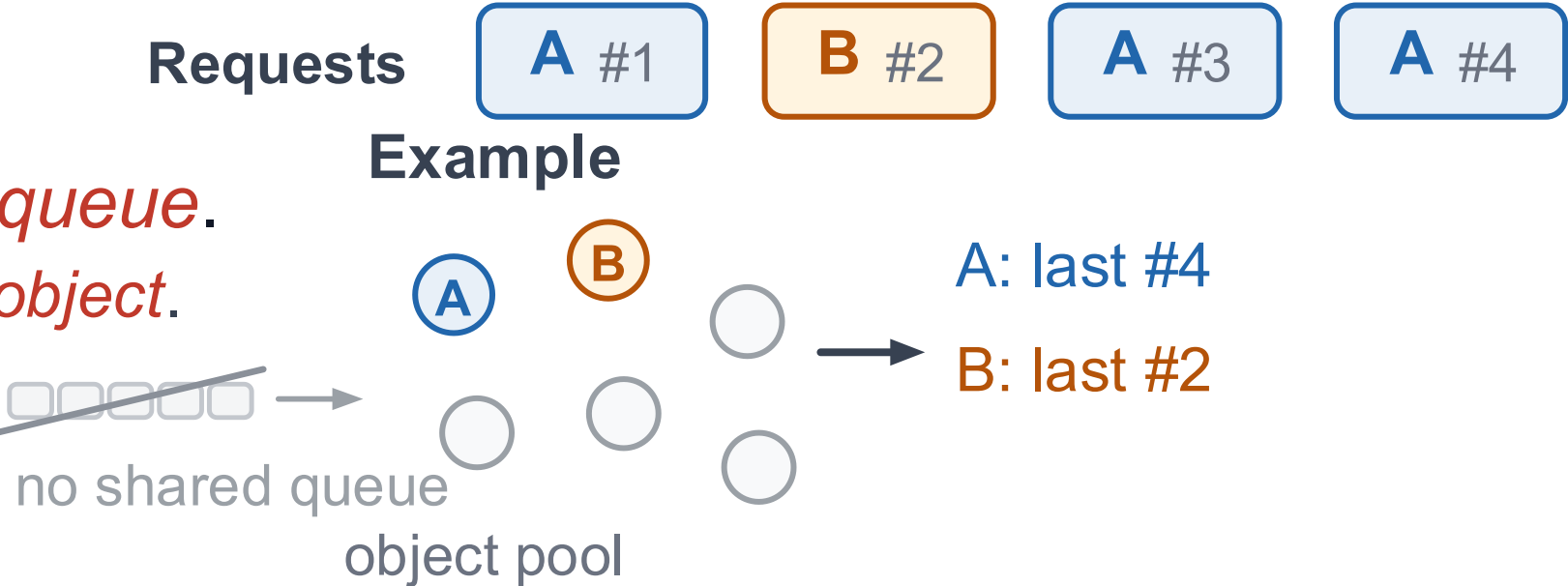
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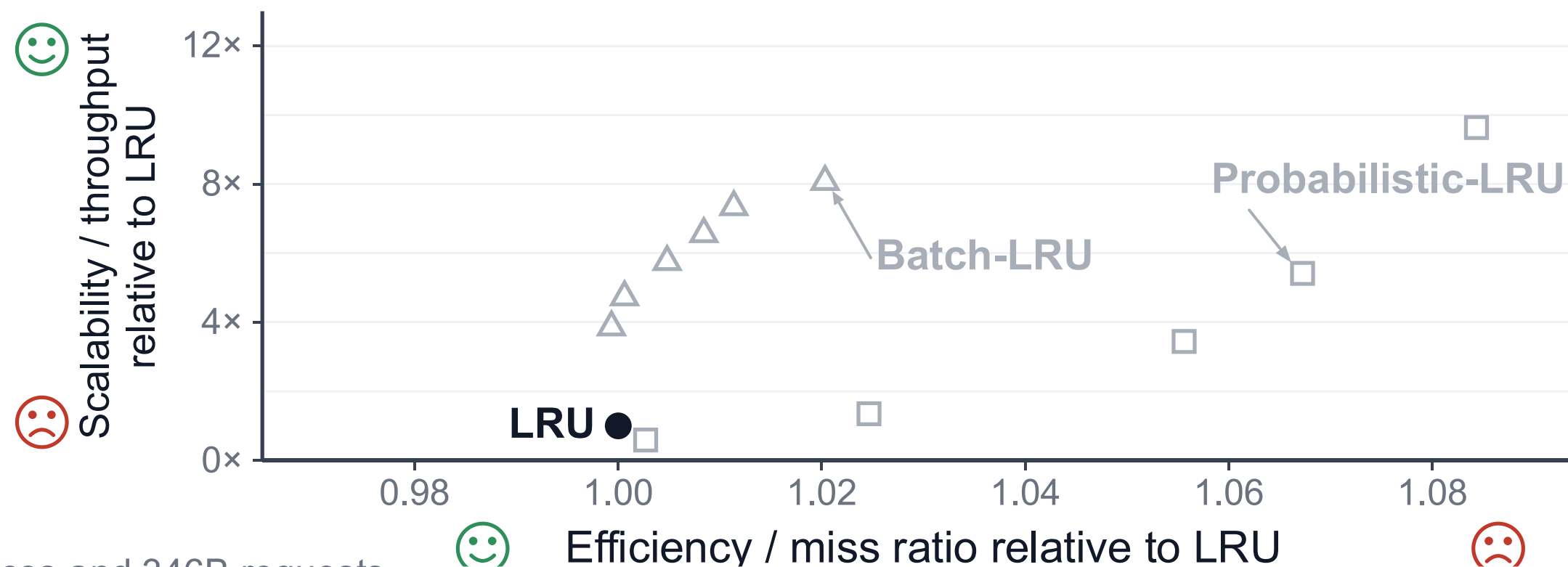
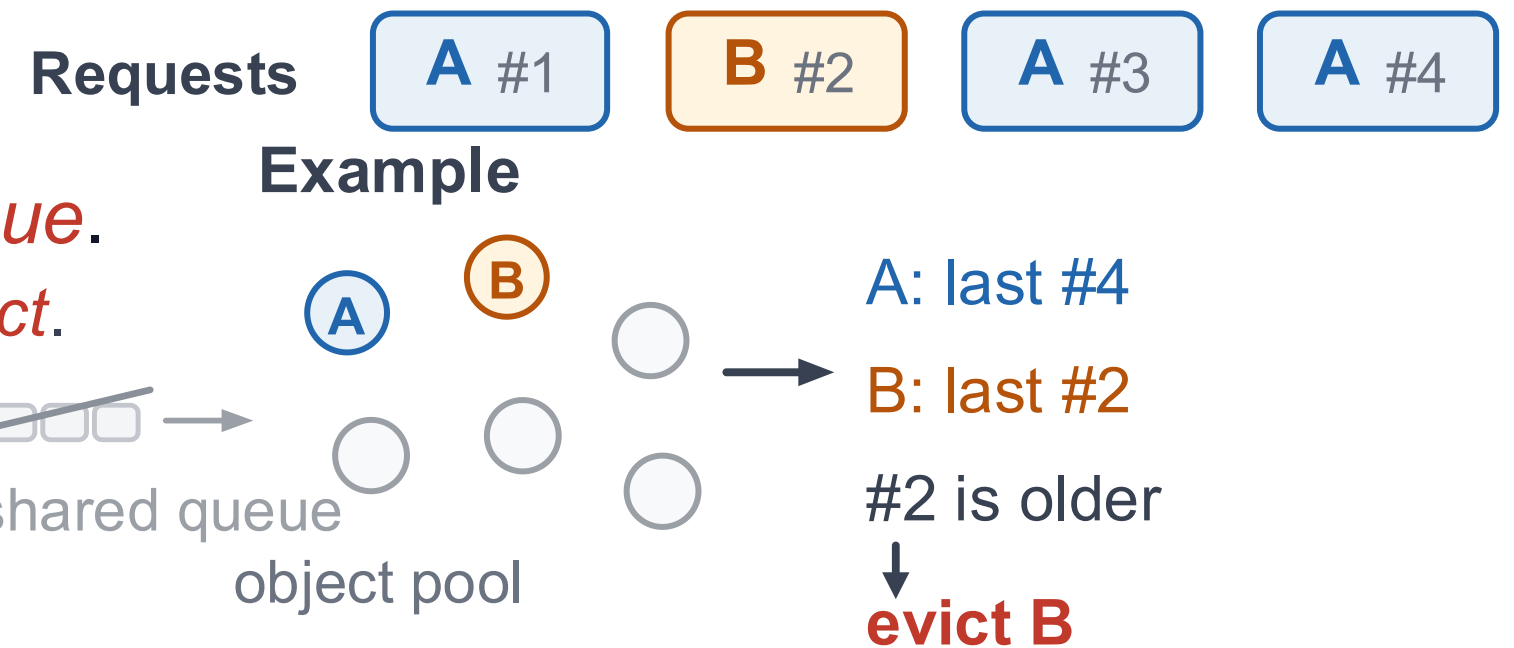


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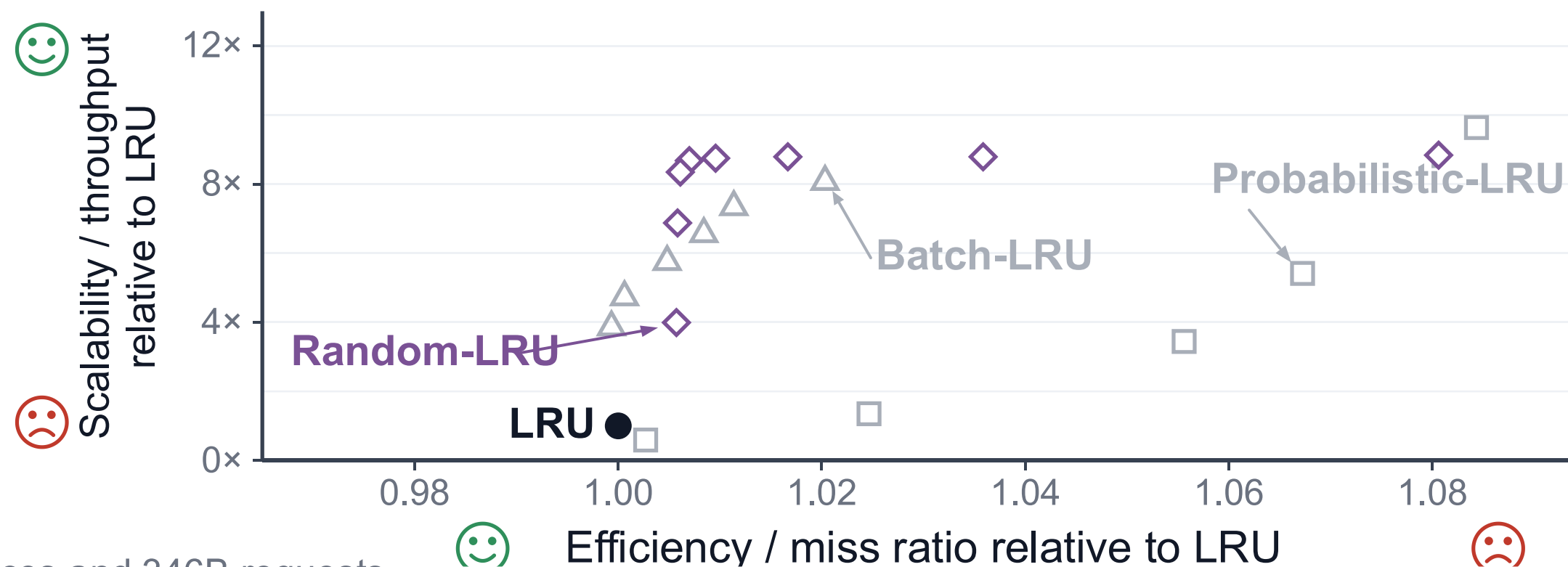
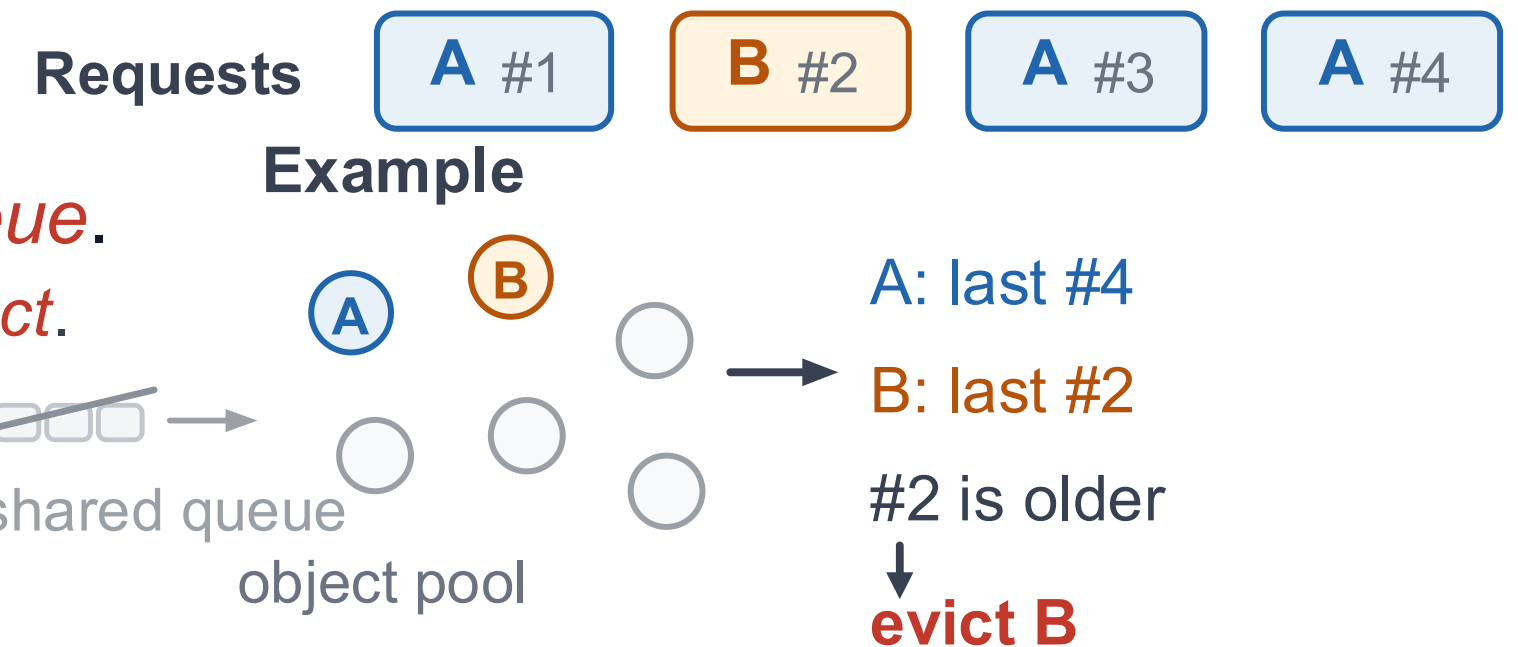


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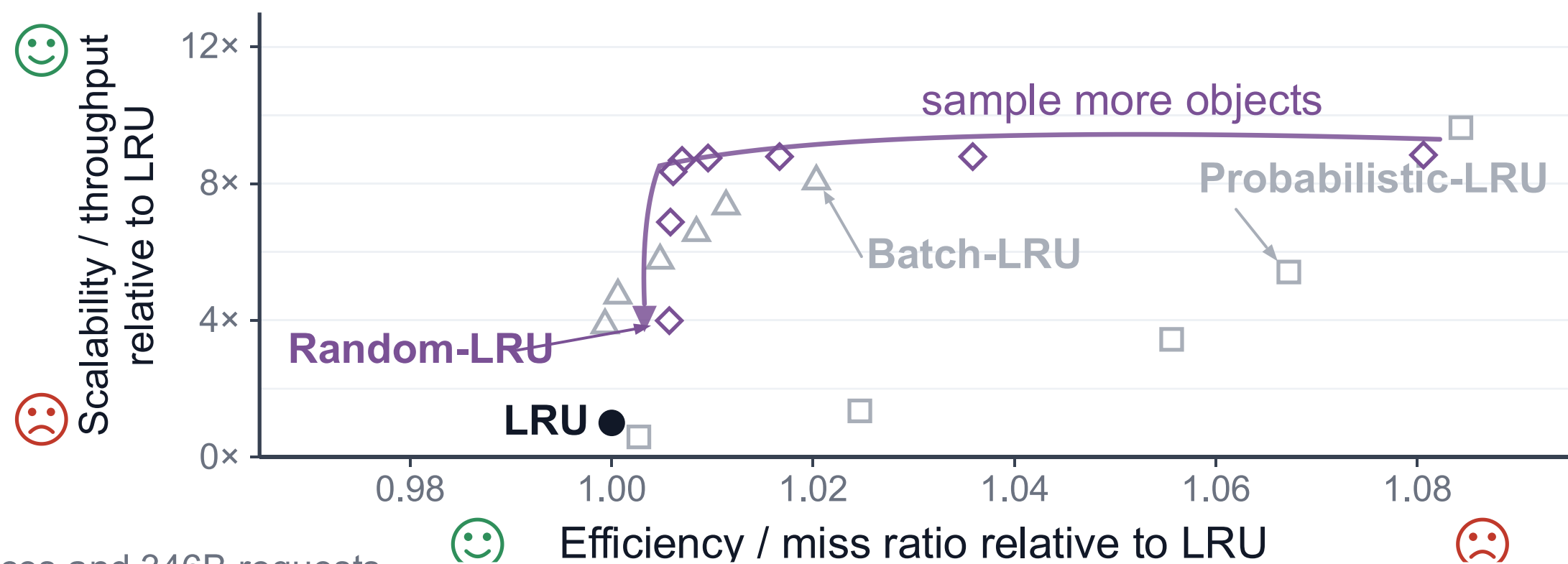
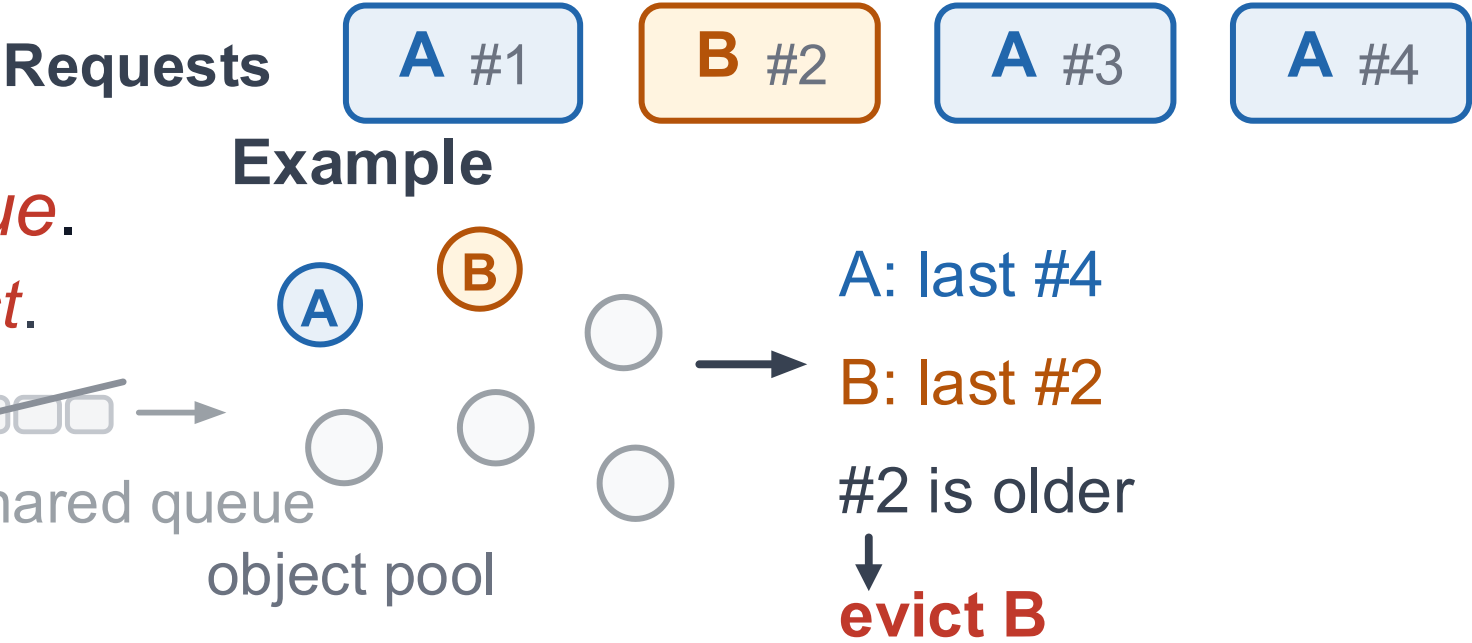
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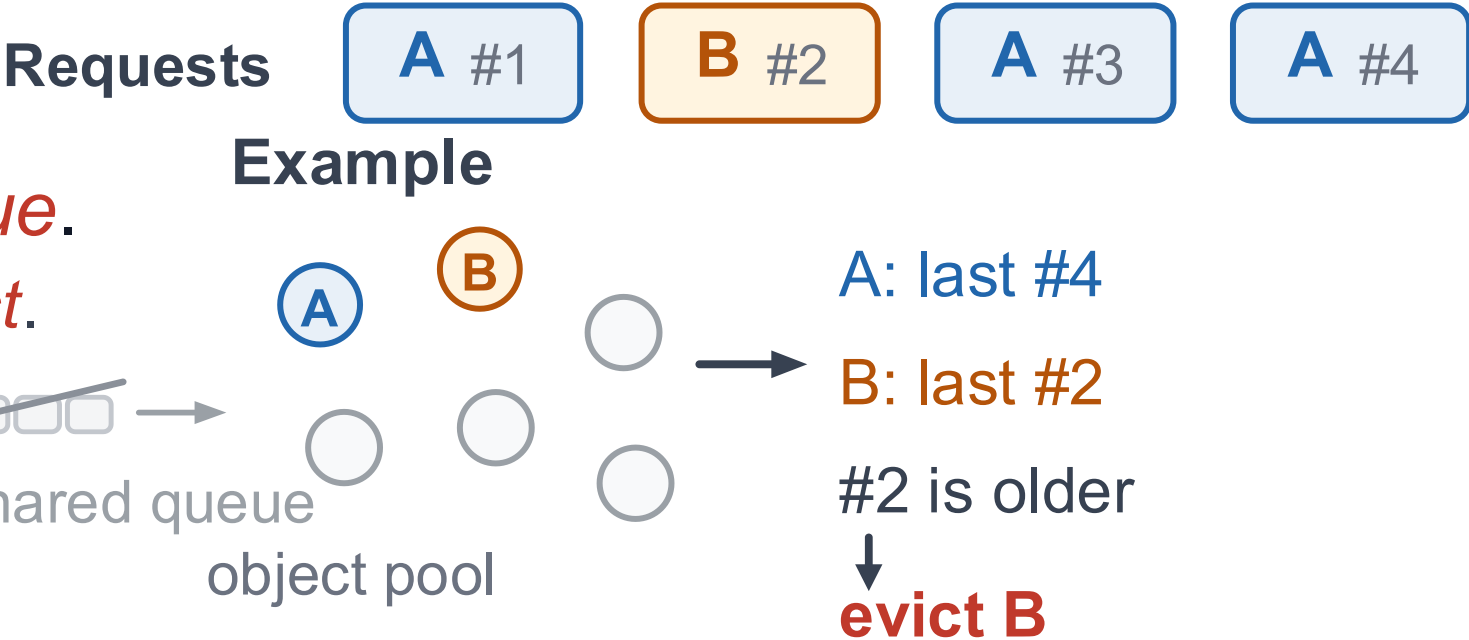
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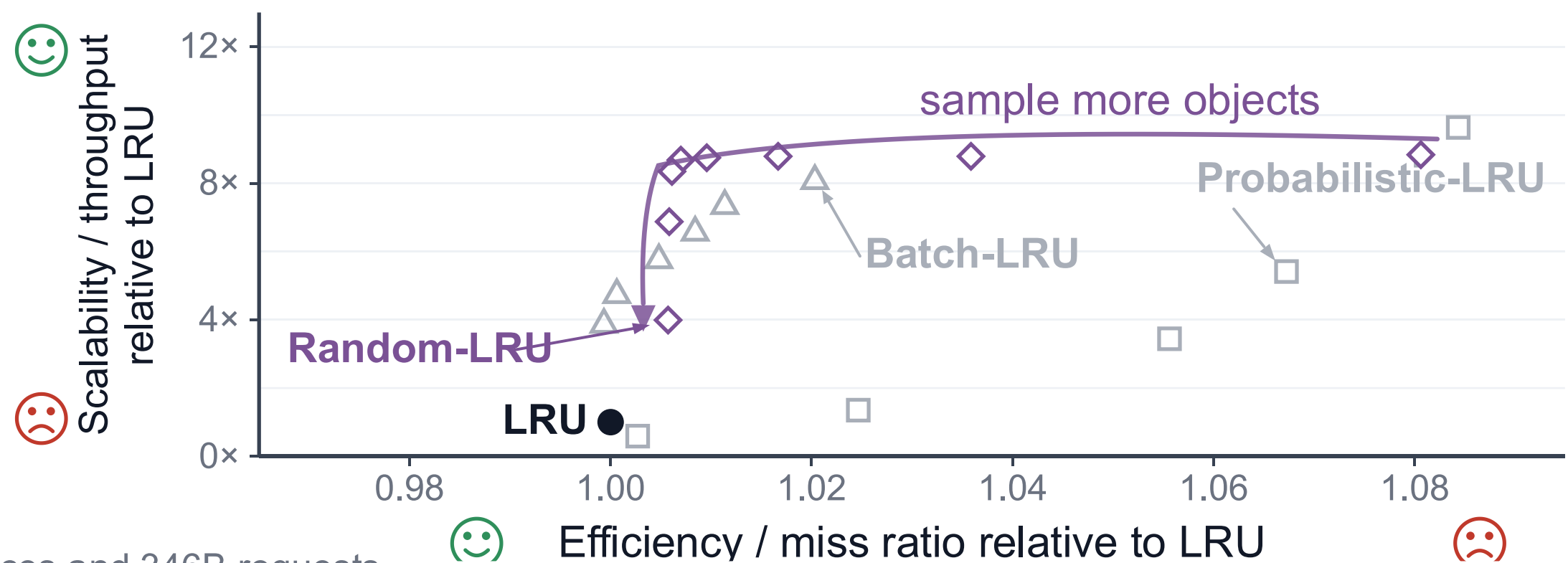
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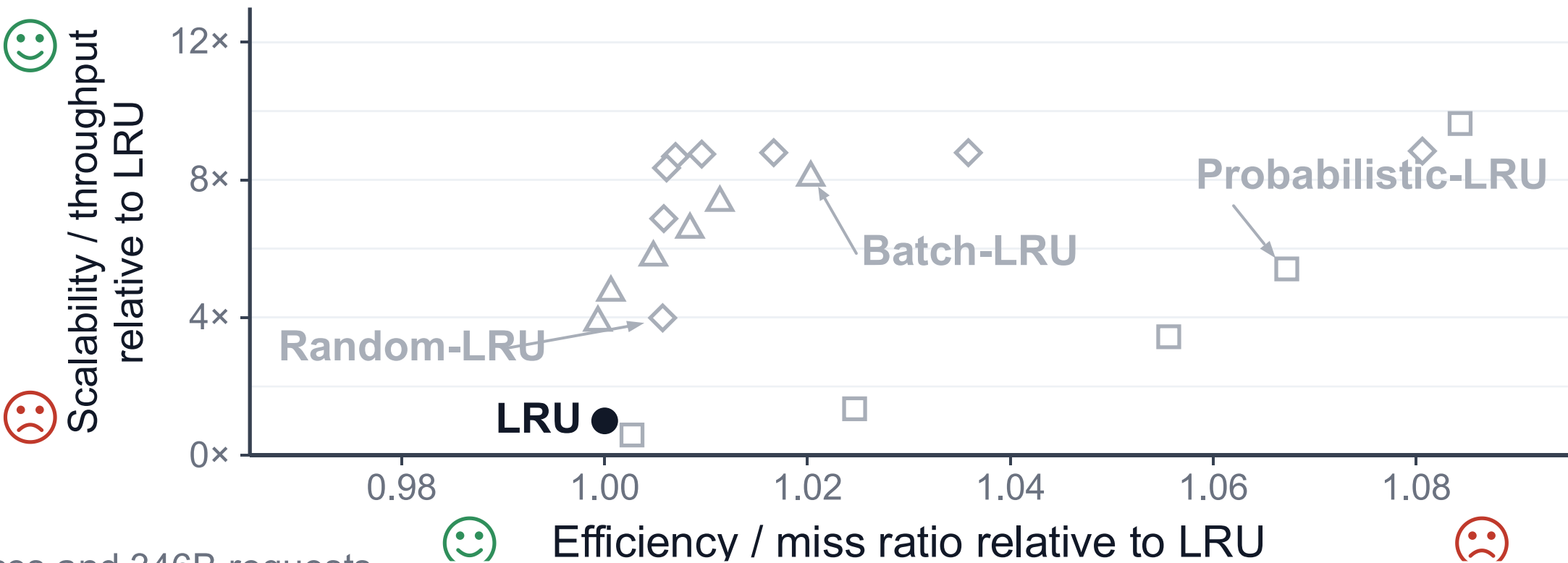
Takeaway: Larger samples approach LRU's miss ratio but lose throughput.



Delay-LRU filters repeated promotions in a window

CacheLib

Requests
Example



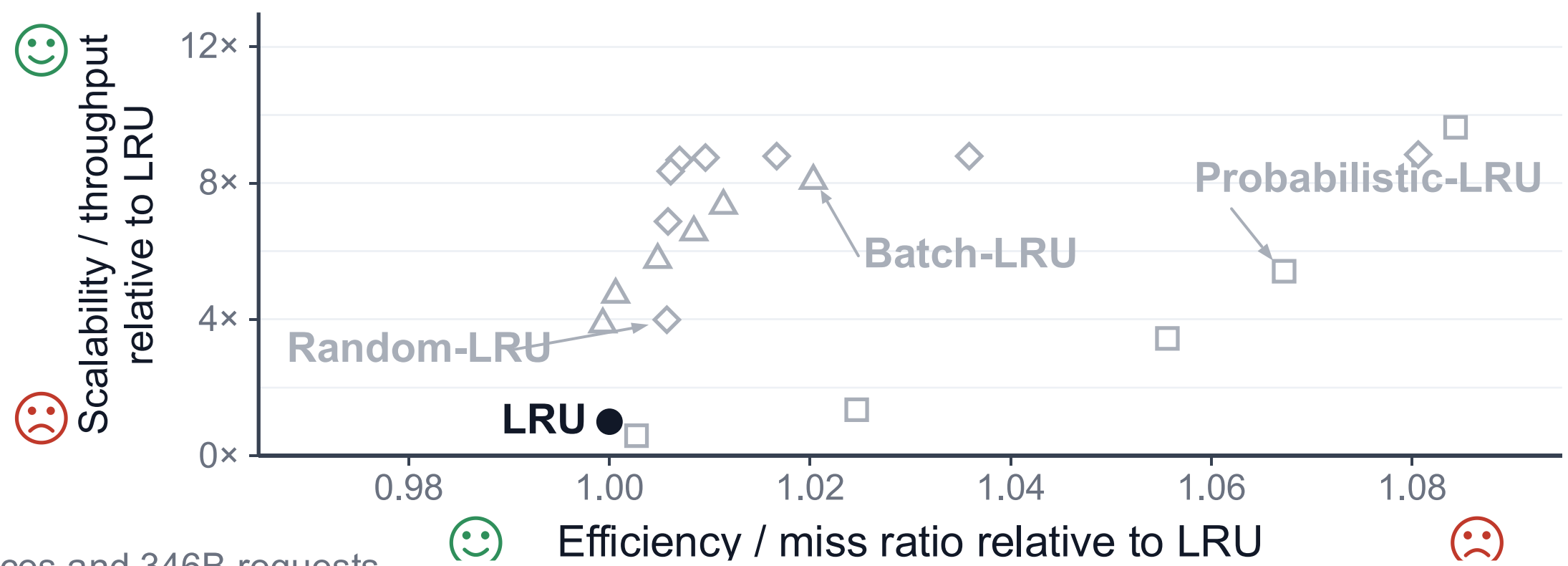
Delay-LRU filters repeated promotions in a window

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Core idea: Start a *delay window* after each promotion.



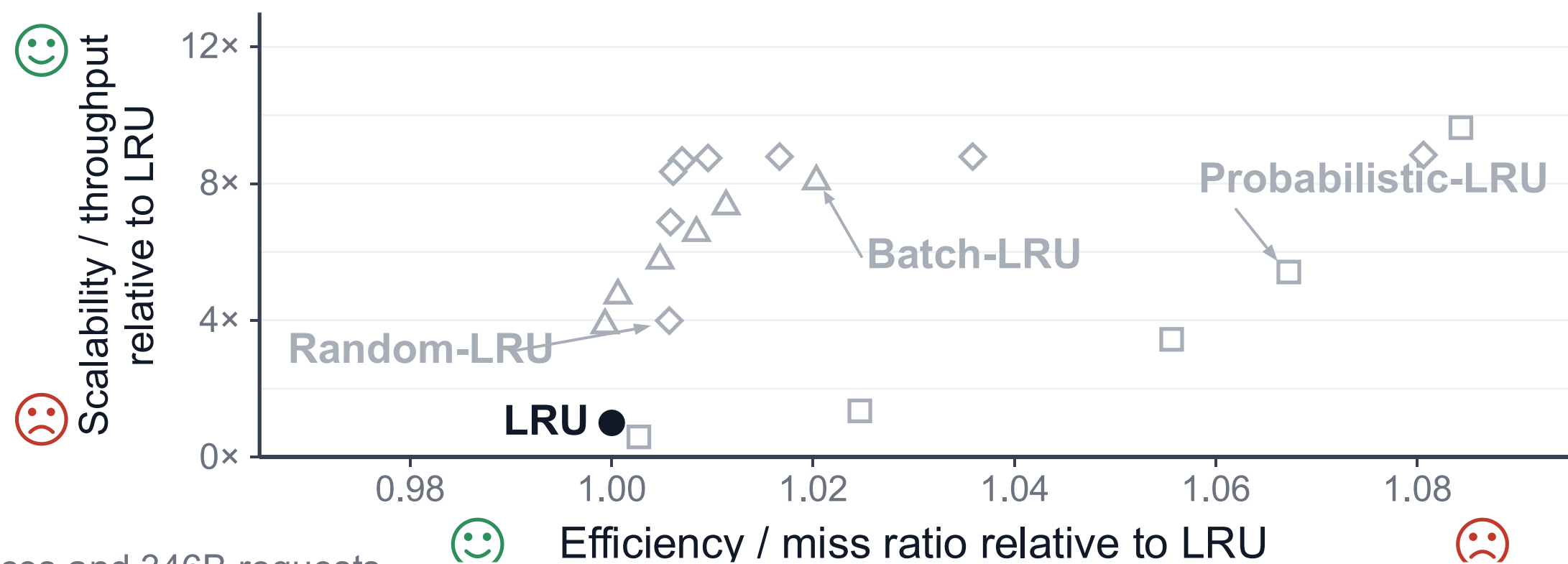
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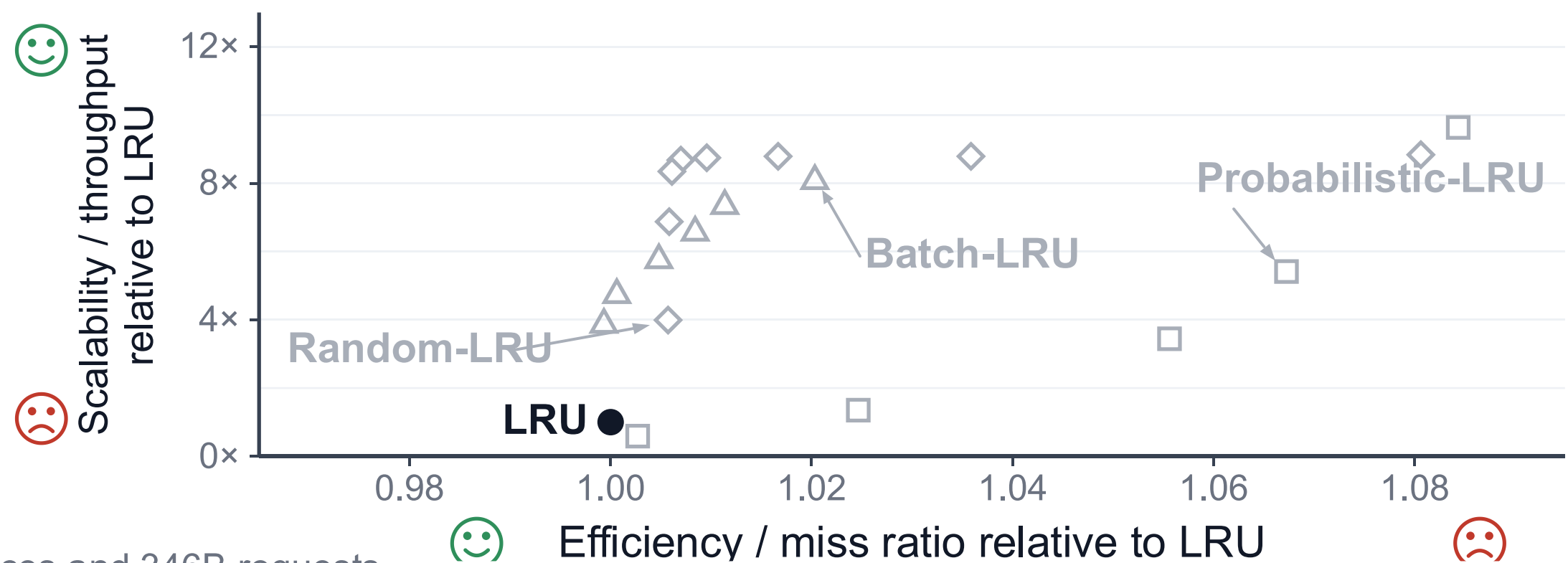
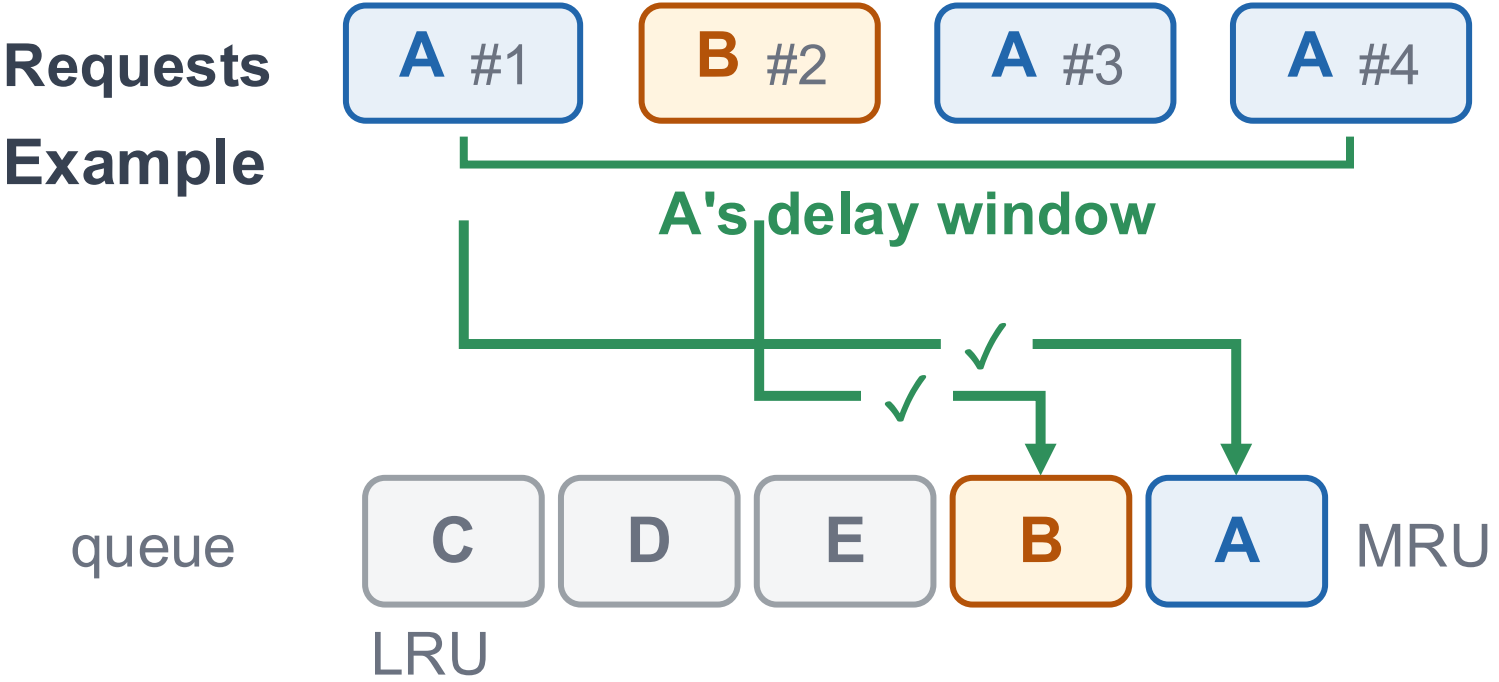
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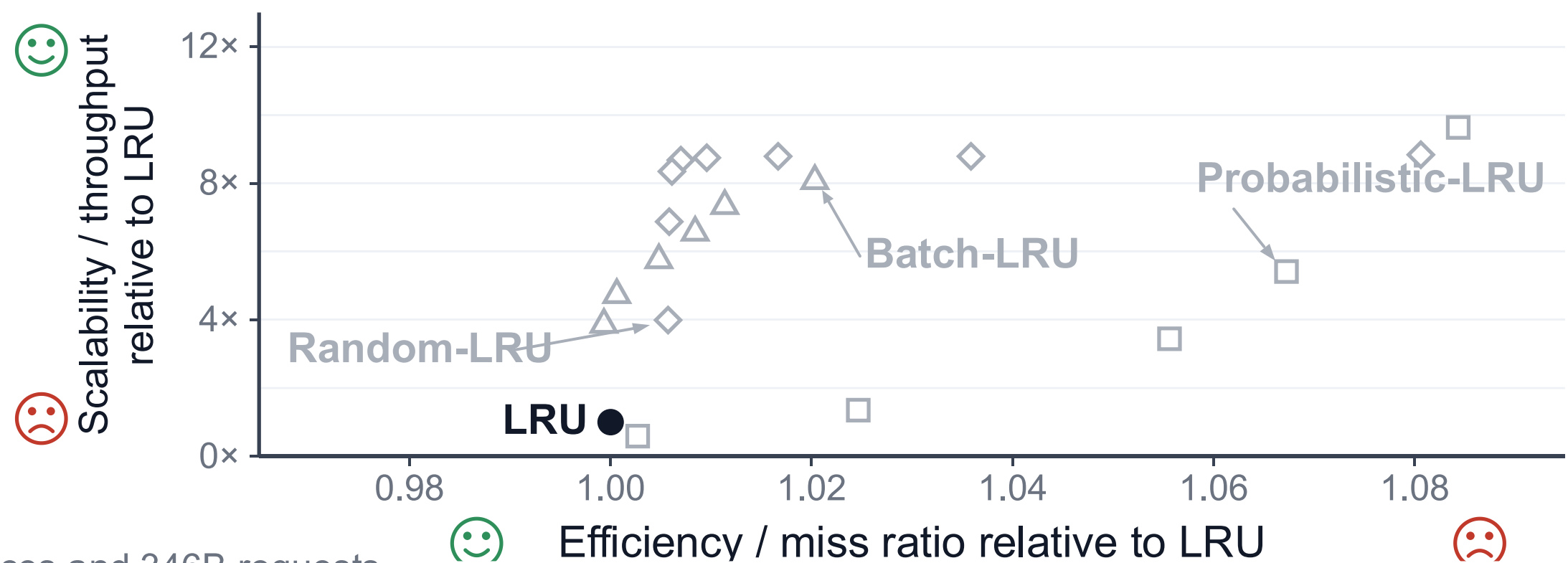
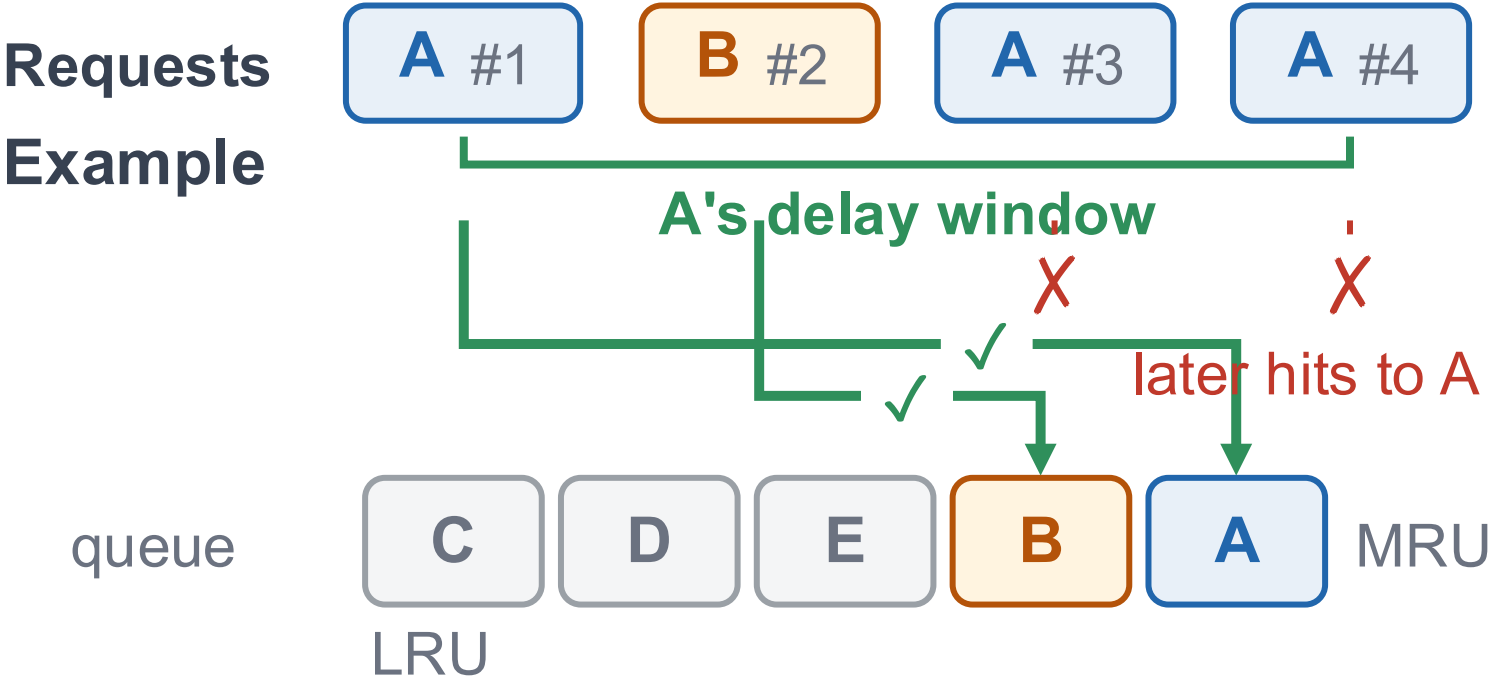
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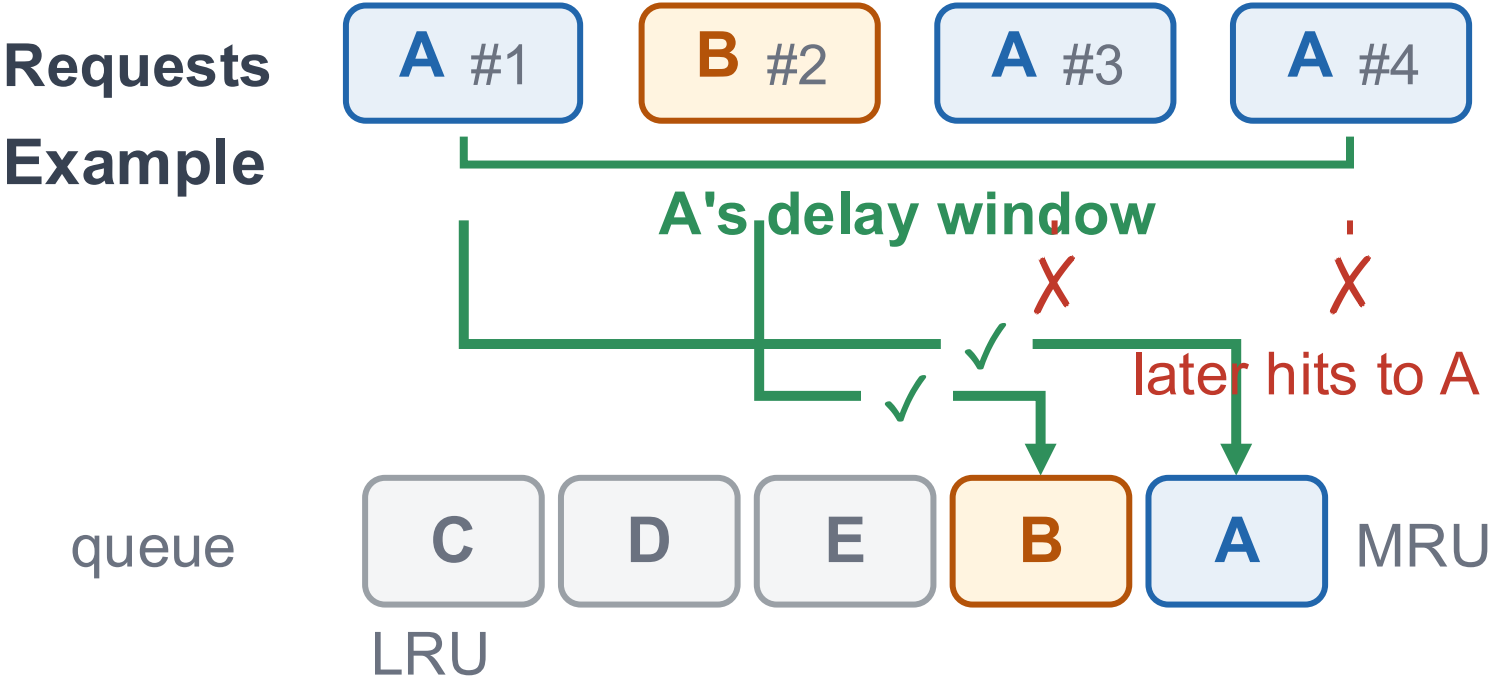
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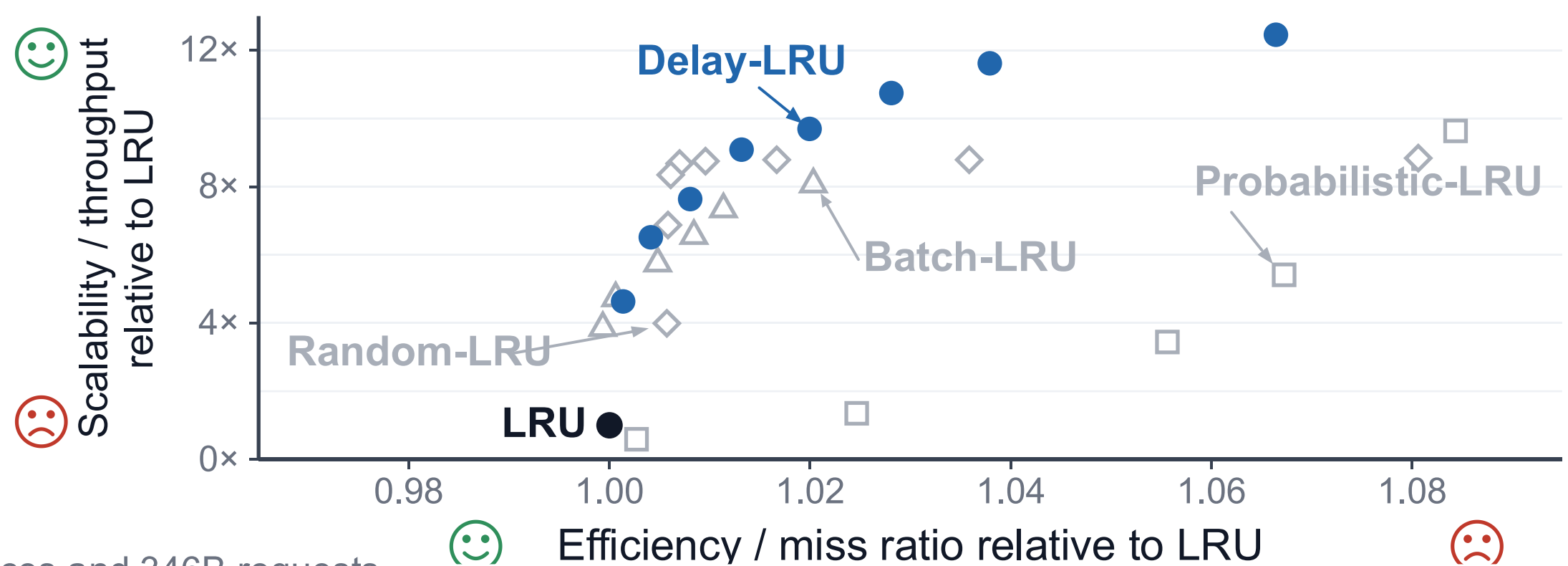
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Takeaway: Among the first four, Delay-LRU offers the best performance trade-off.

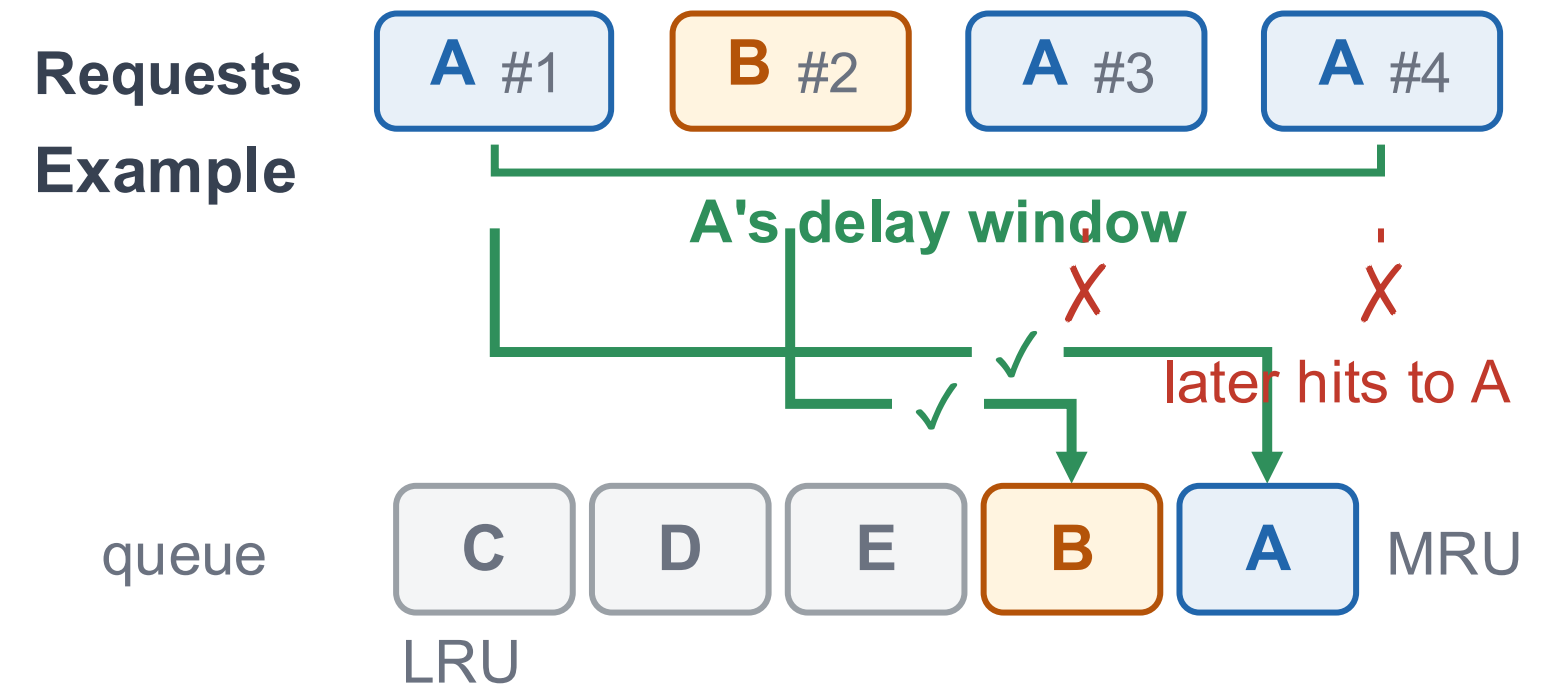


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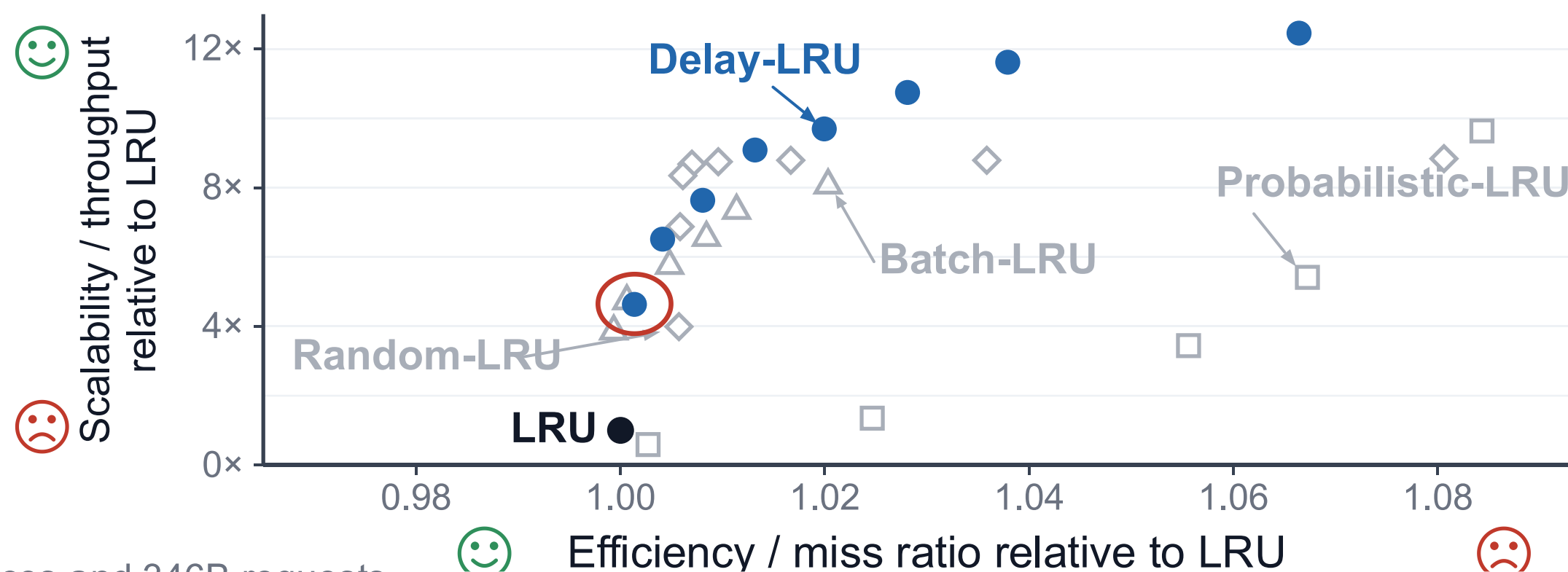
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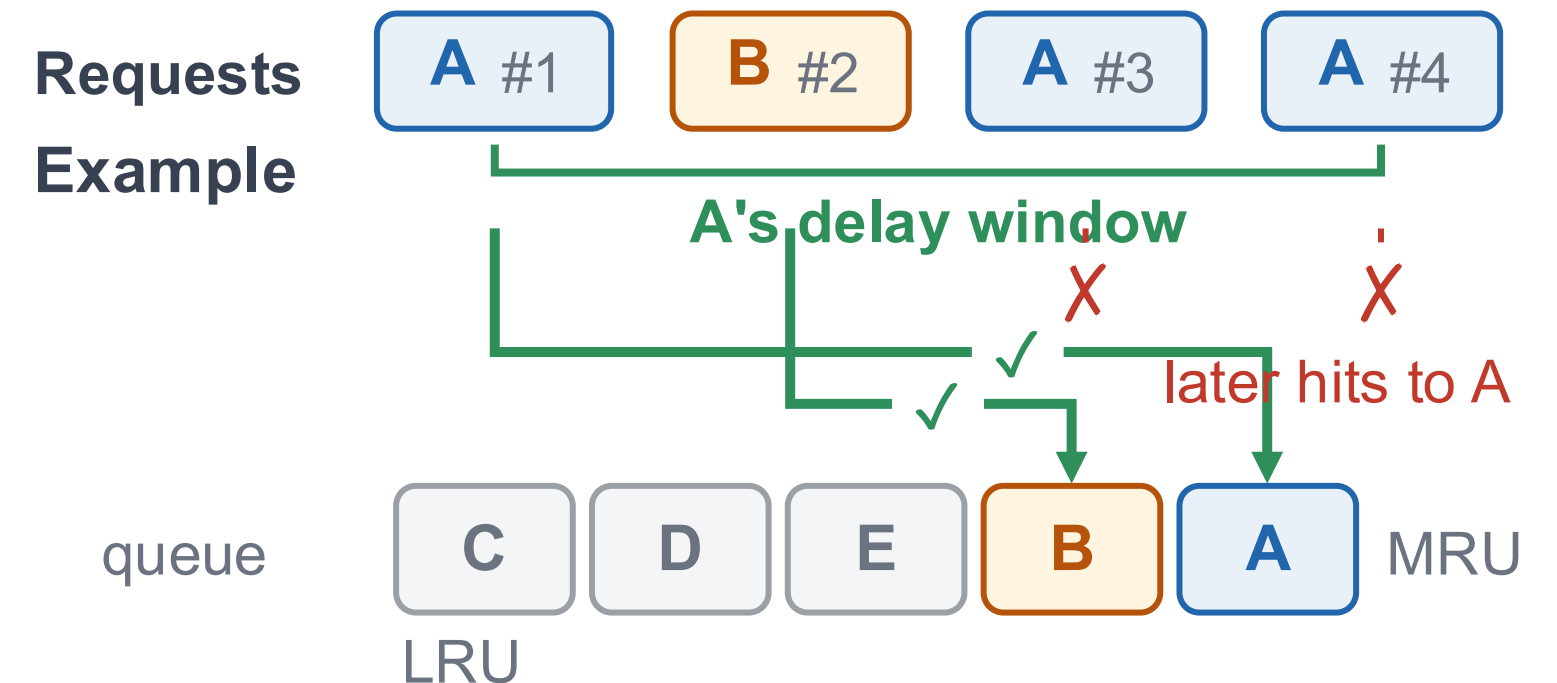


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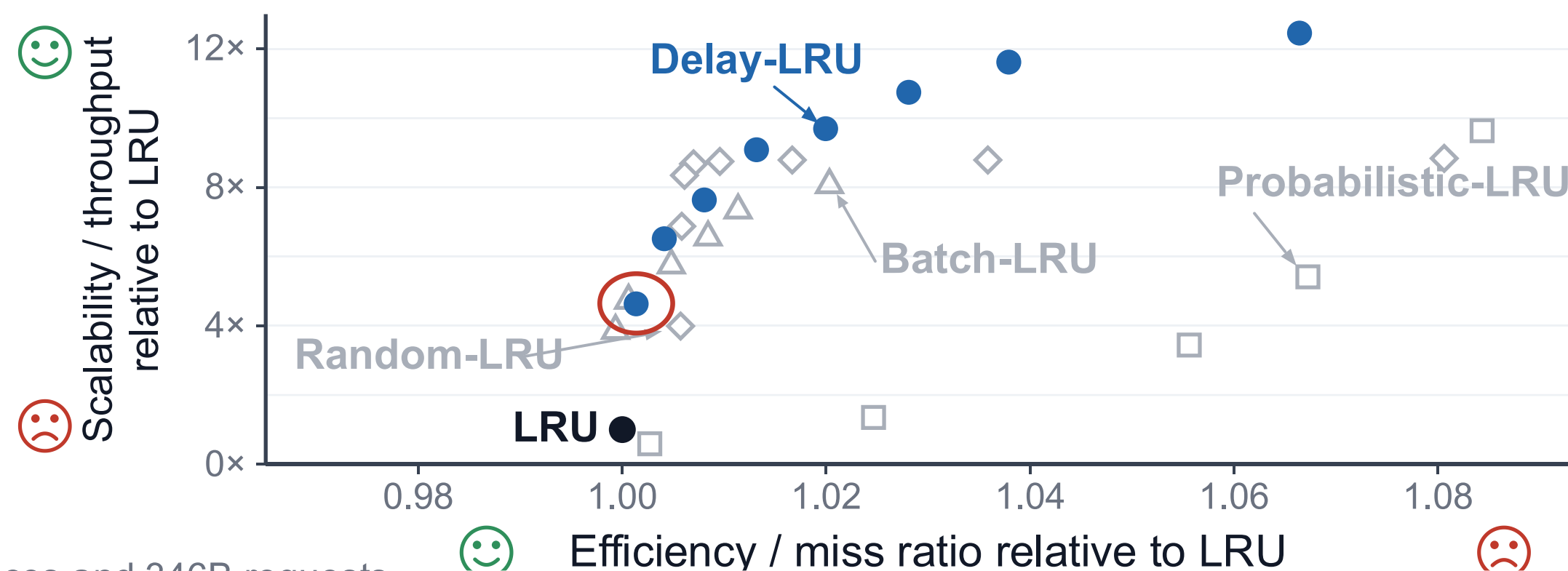
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Our conjecture: Filtering repeated hits to one object removes redundant promotions.

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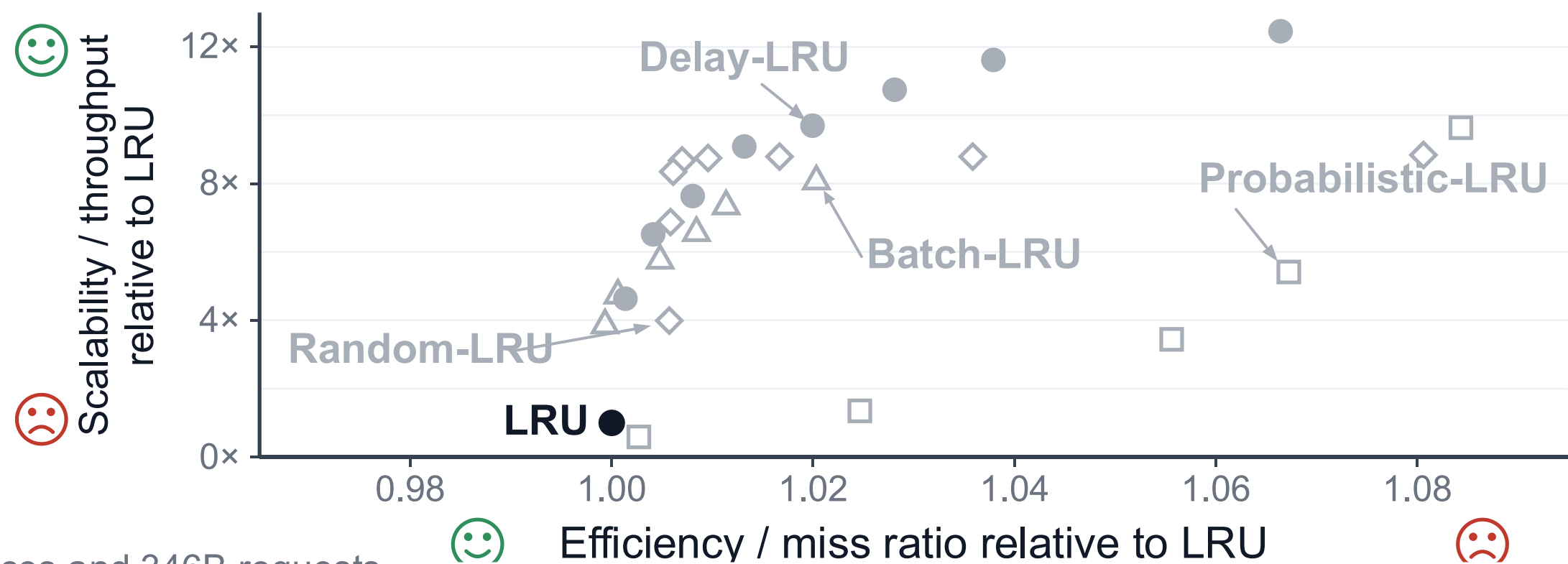


FIFO-reinsertion decides at eviction

Simplified pseudocode

```
on hit(x) :  
    marked[x] = true
```

Related: [QDLP](#) · [S3-FIFO](#) · [SIEVE](#)

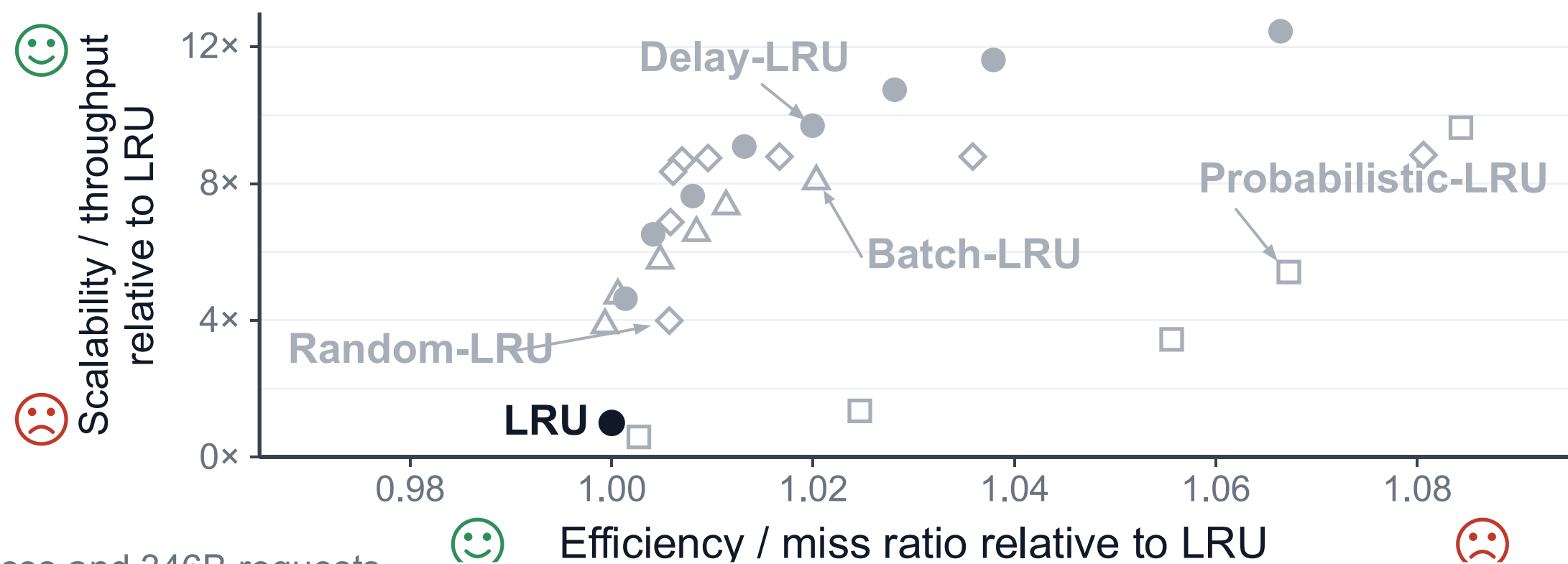


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Simplified pseudocode

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on eviction:  
    while front is marked: clear; move to back  
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Related: [QDLP](#) · [S3-FIFO](#) · [SIEVE](#)



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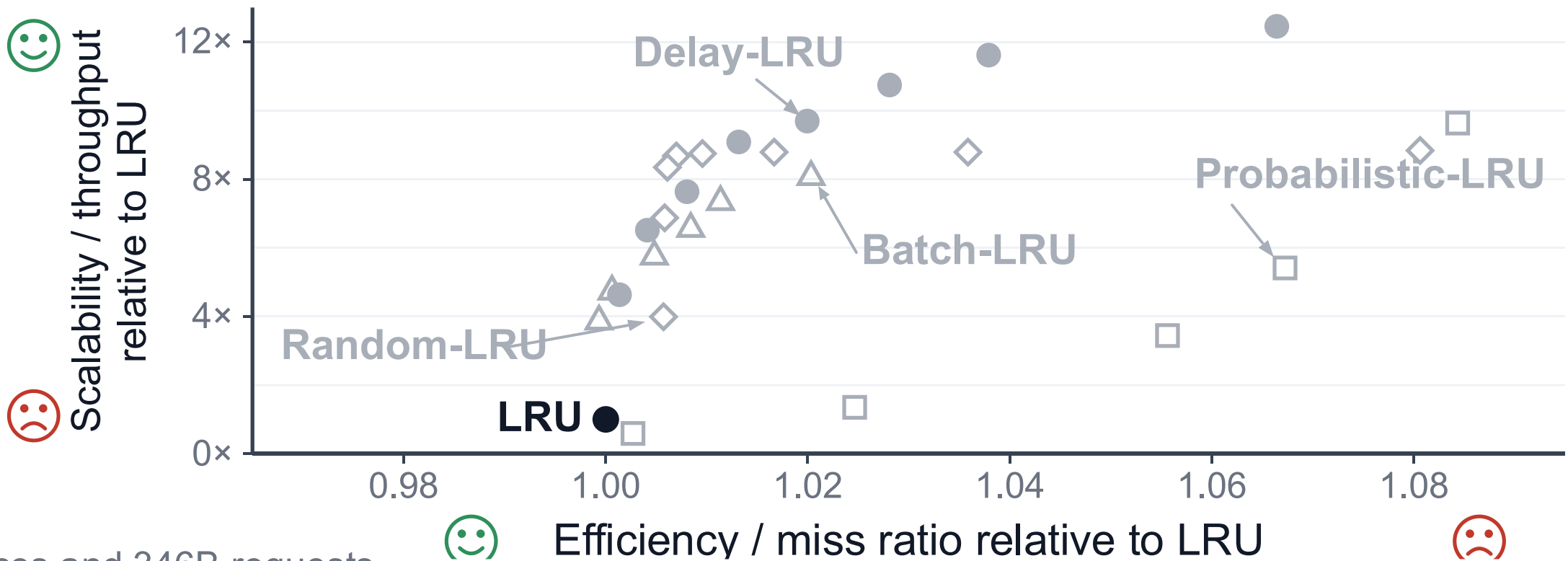
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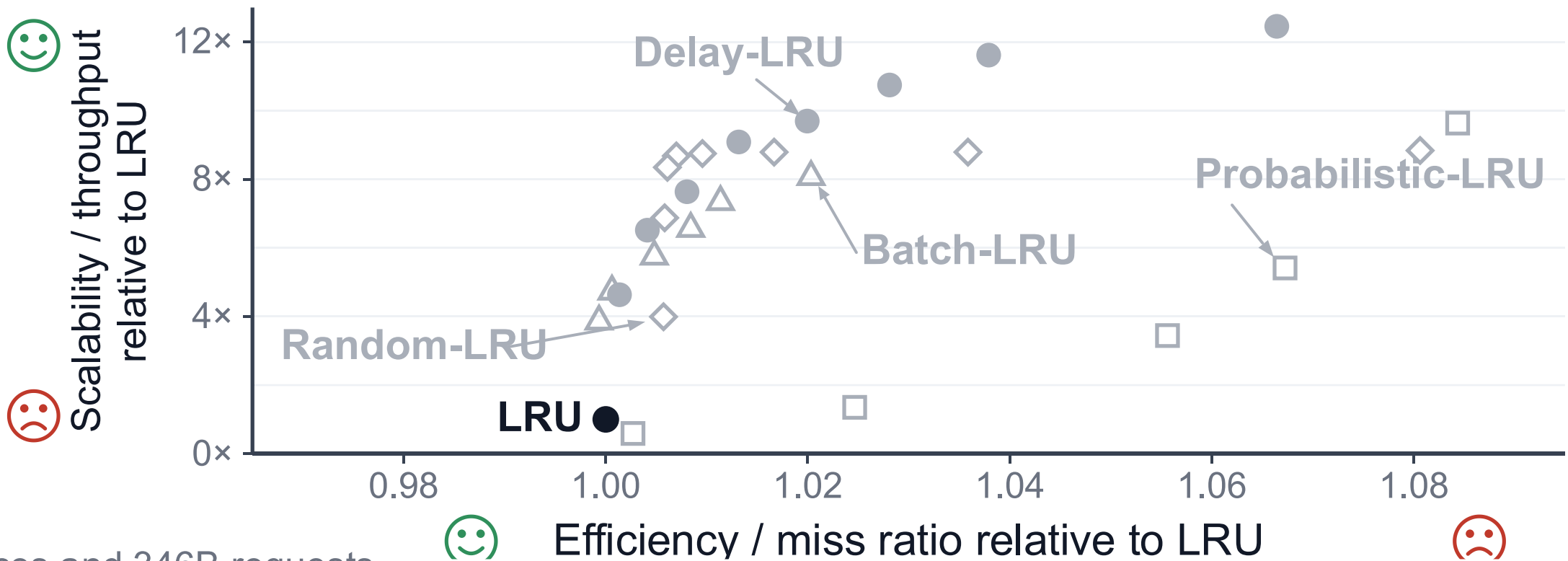
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```

Example



cache miss on X

Related: [QDLP](#) · [S3-FIFO](#) · [SIEVE](#)

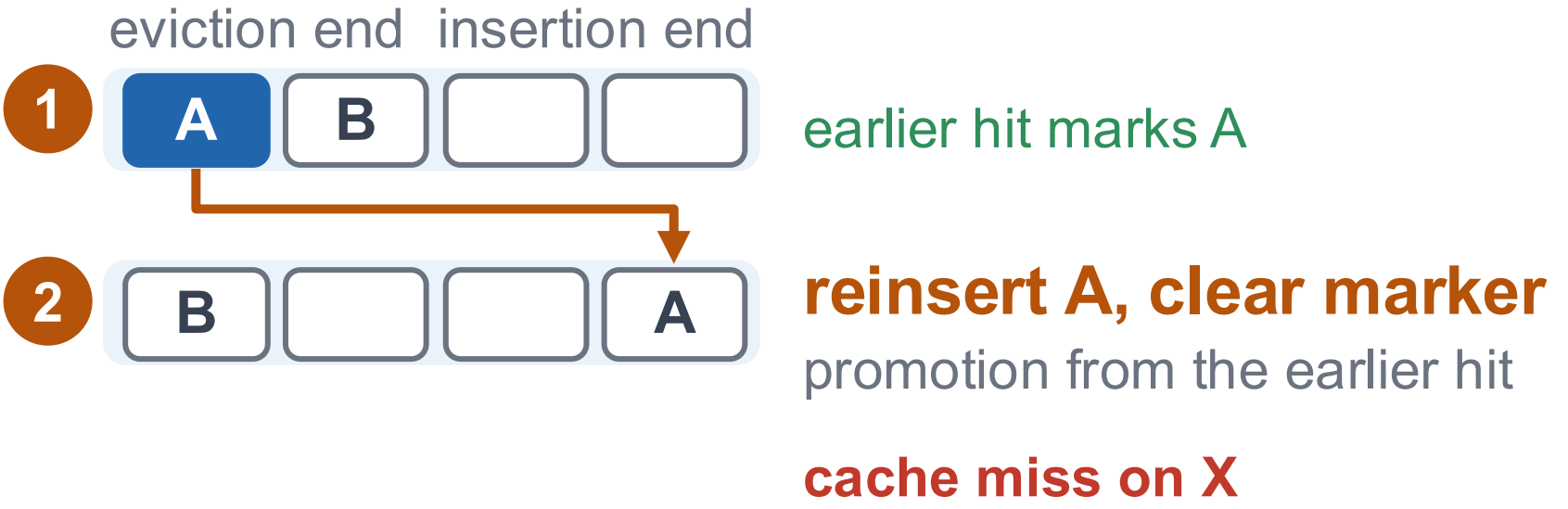


FIFO-reinsertion decides at eviction

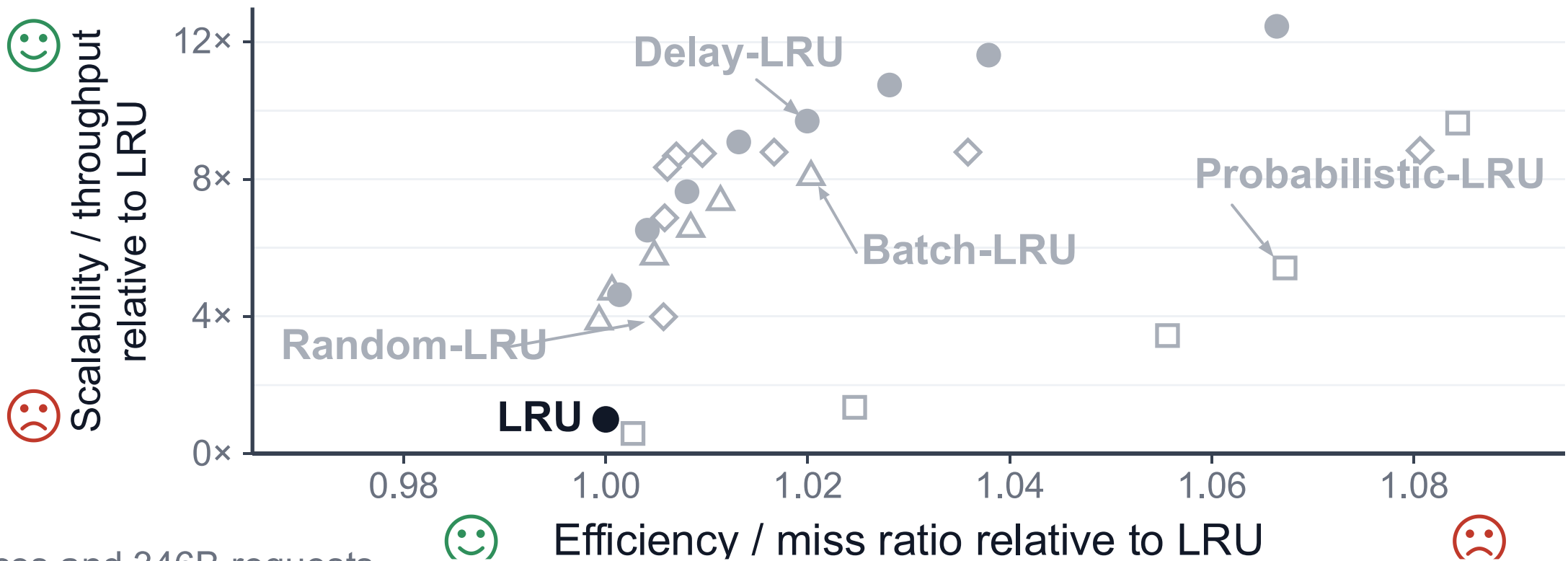
Simplified pseudocode

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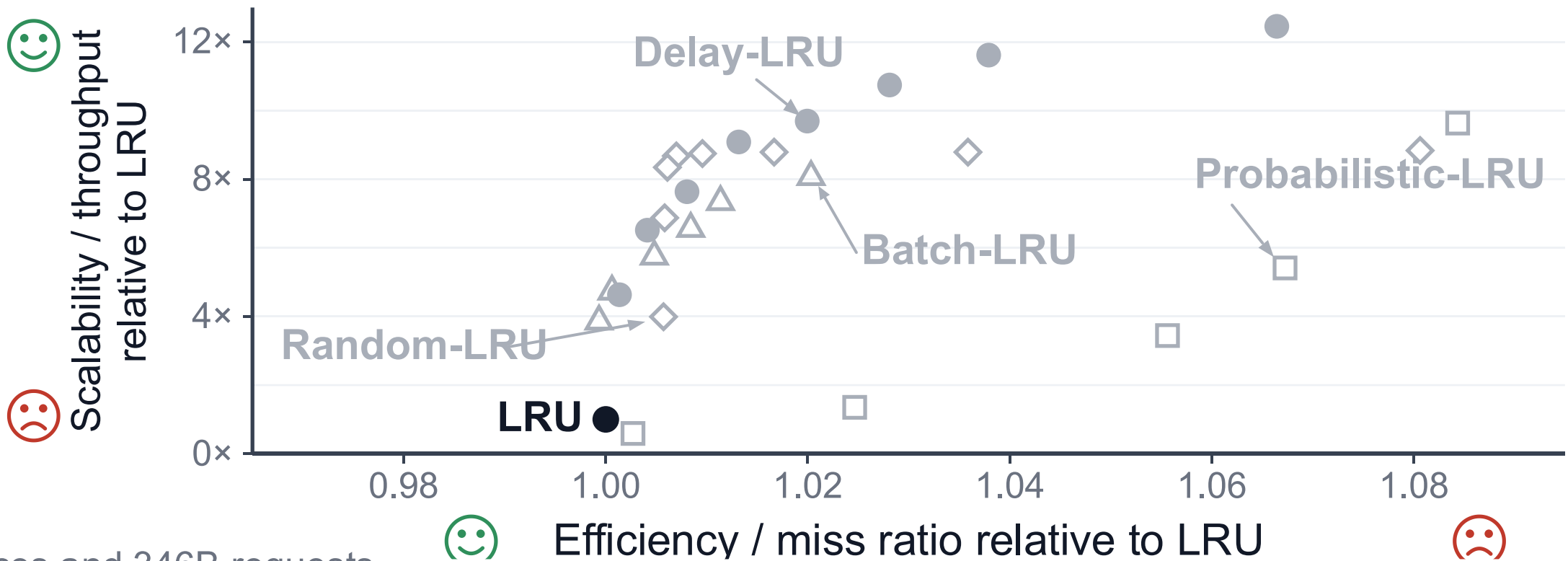
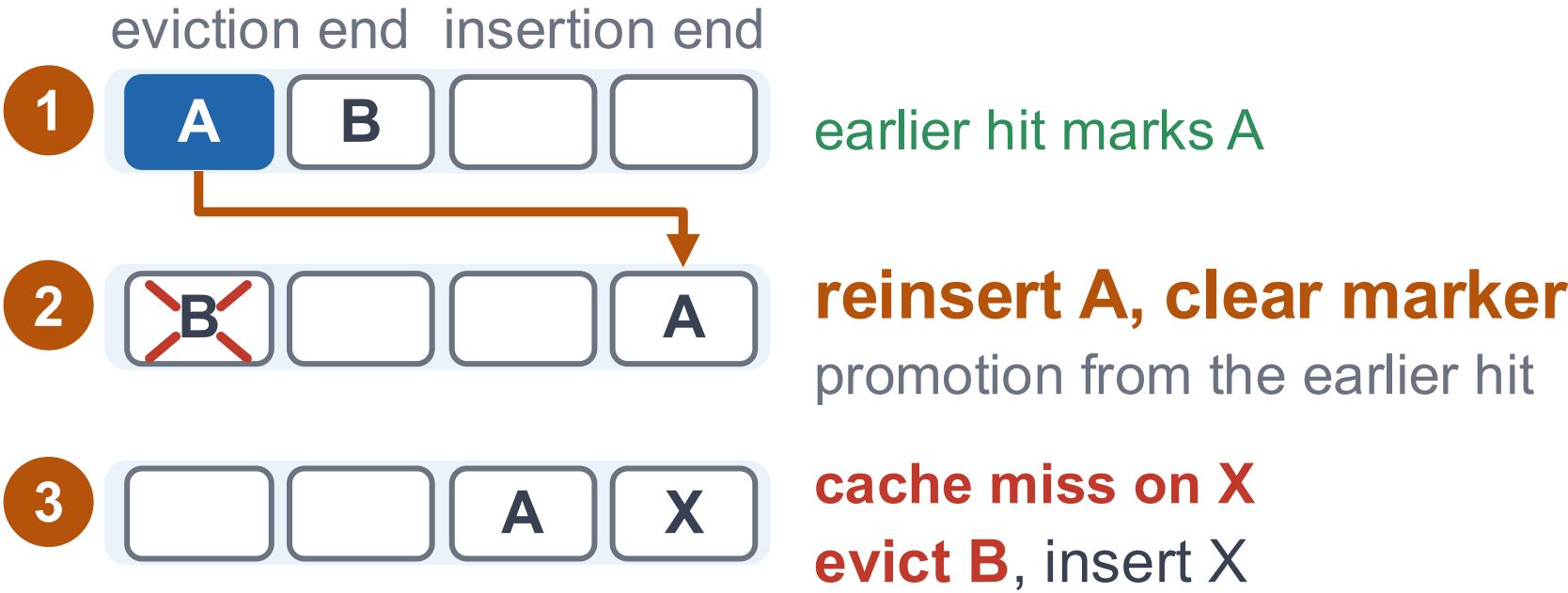
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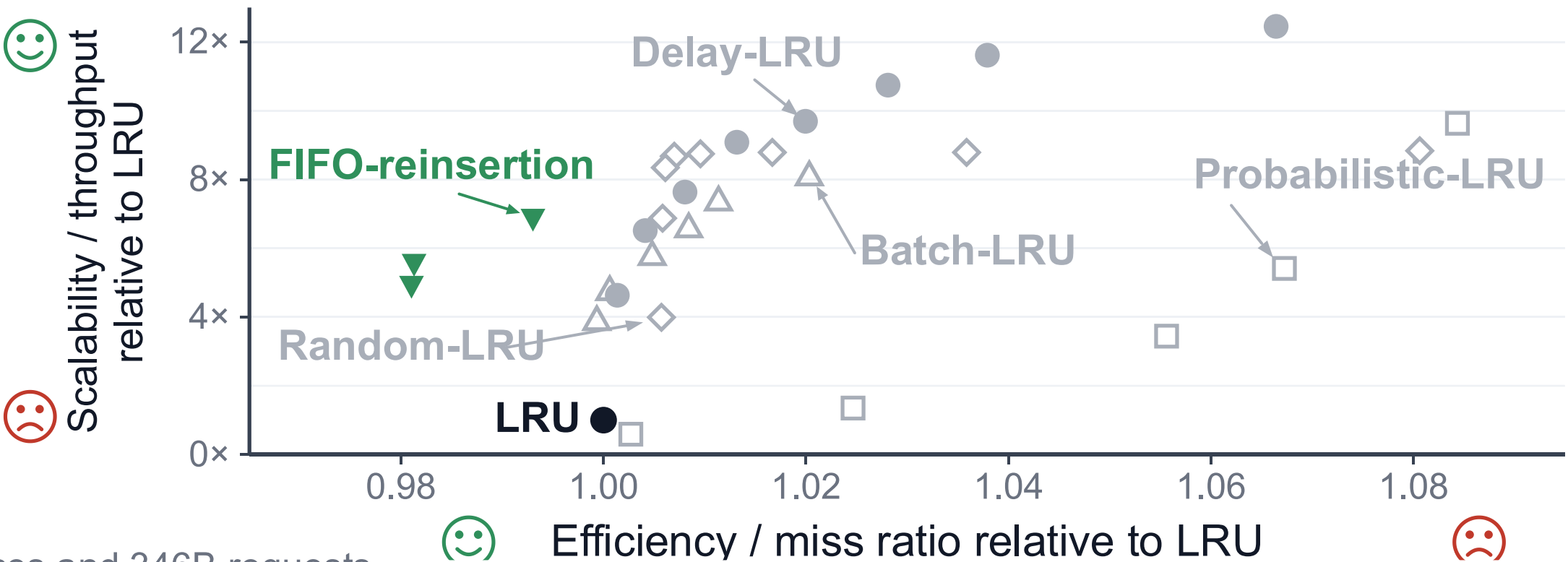
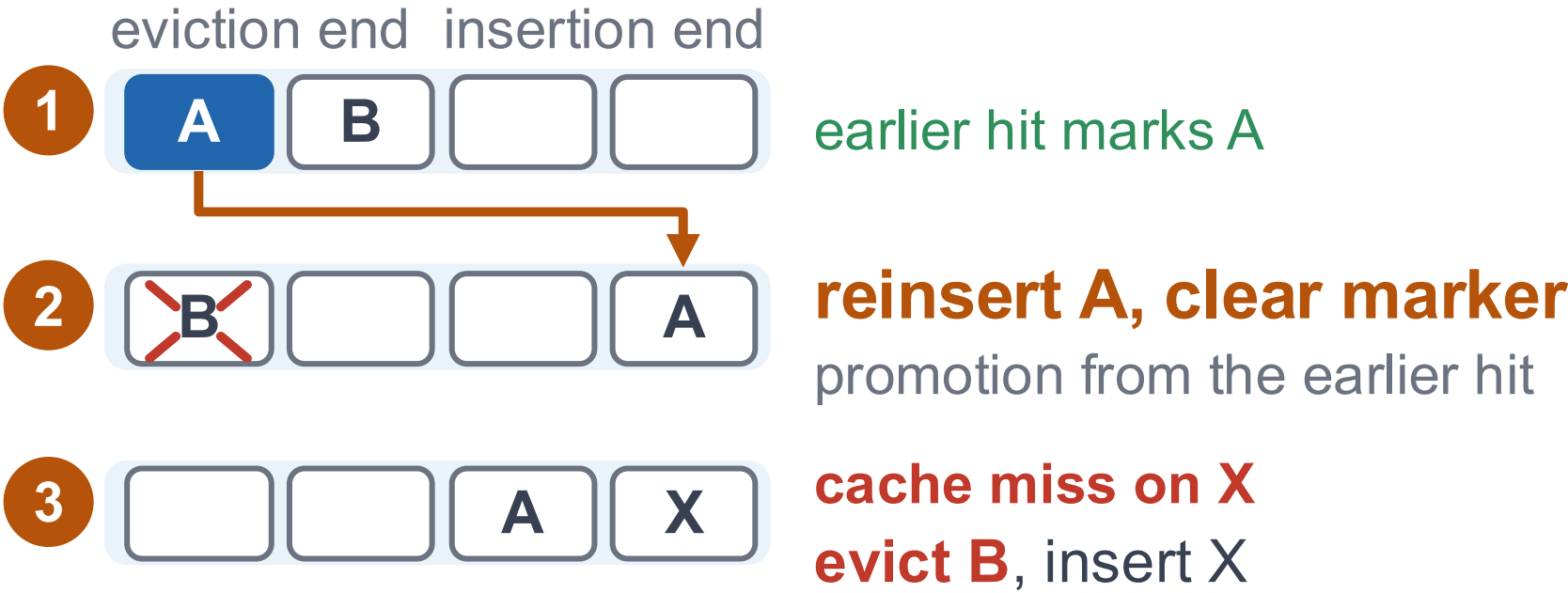
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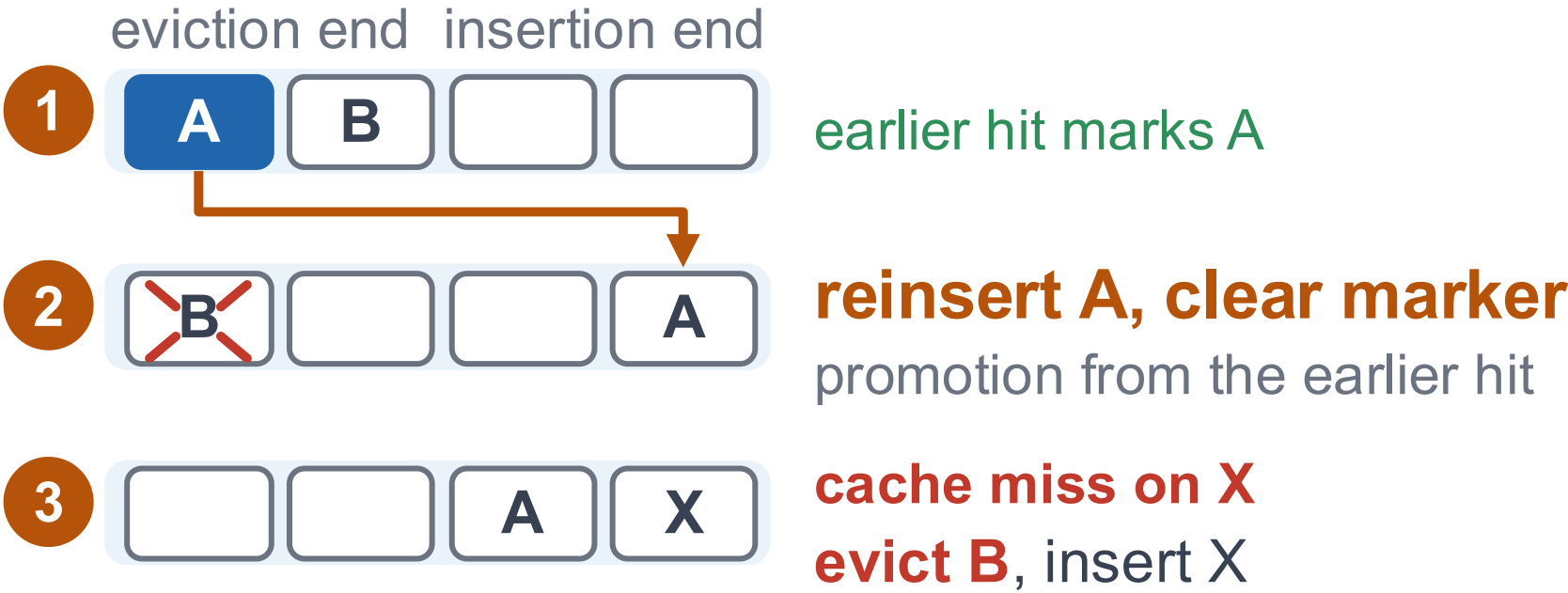


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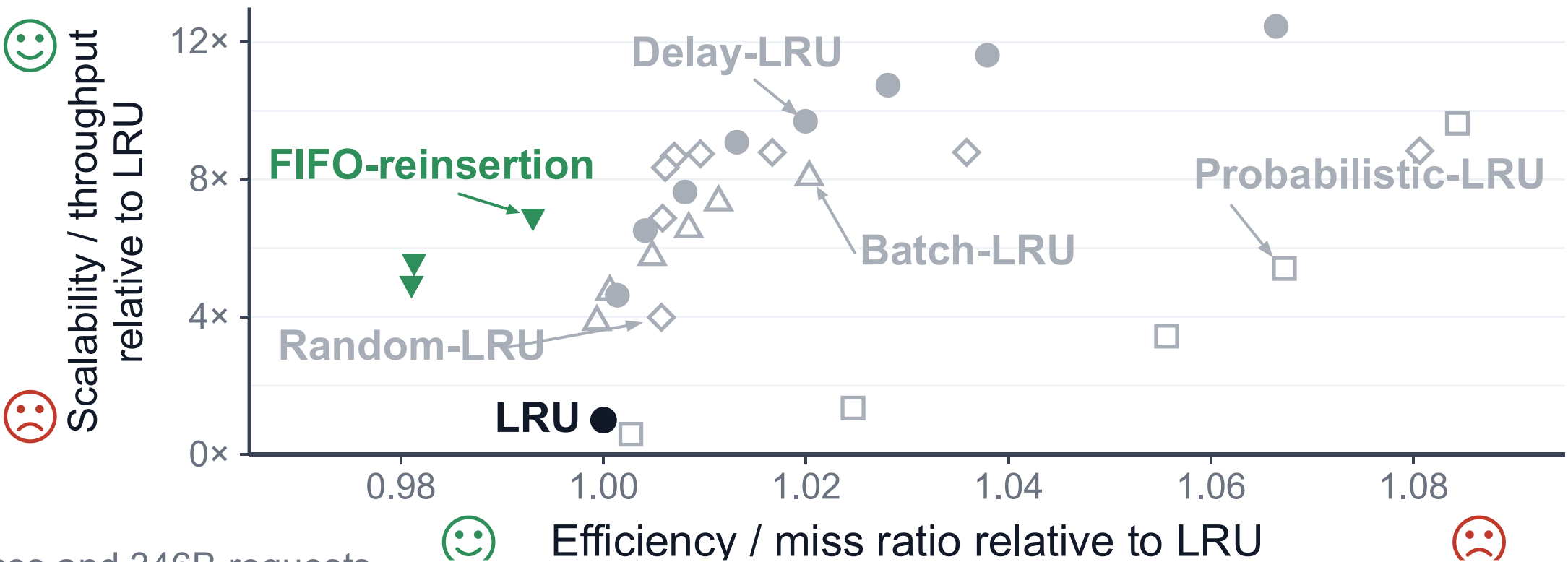
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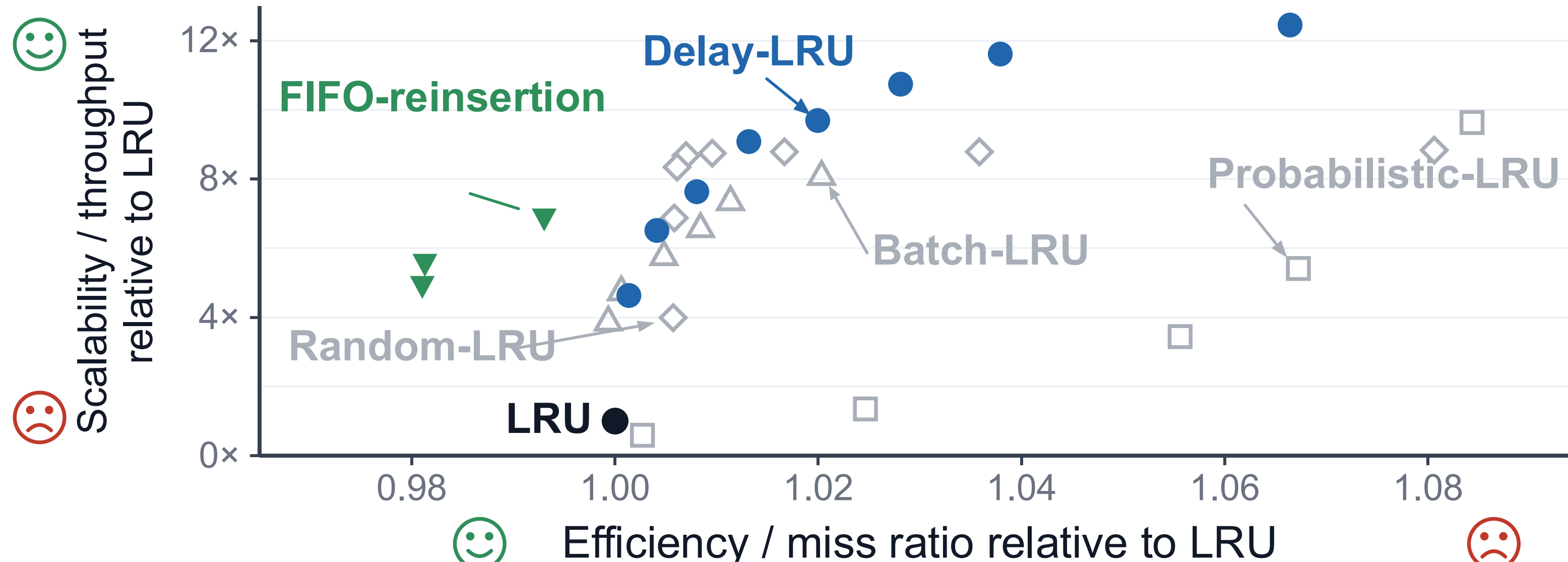


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Takeaway: FIFO-reinsertion improves both throughput and miss ratio vs. LRU.

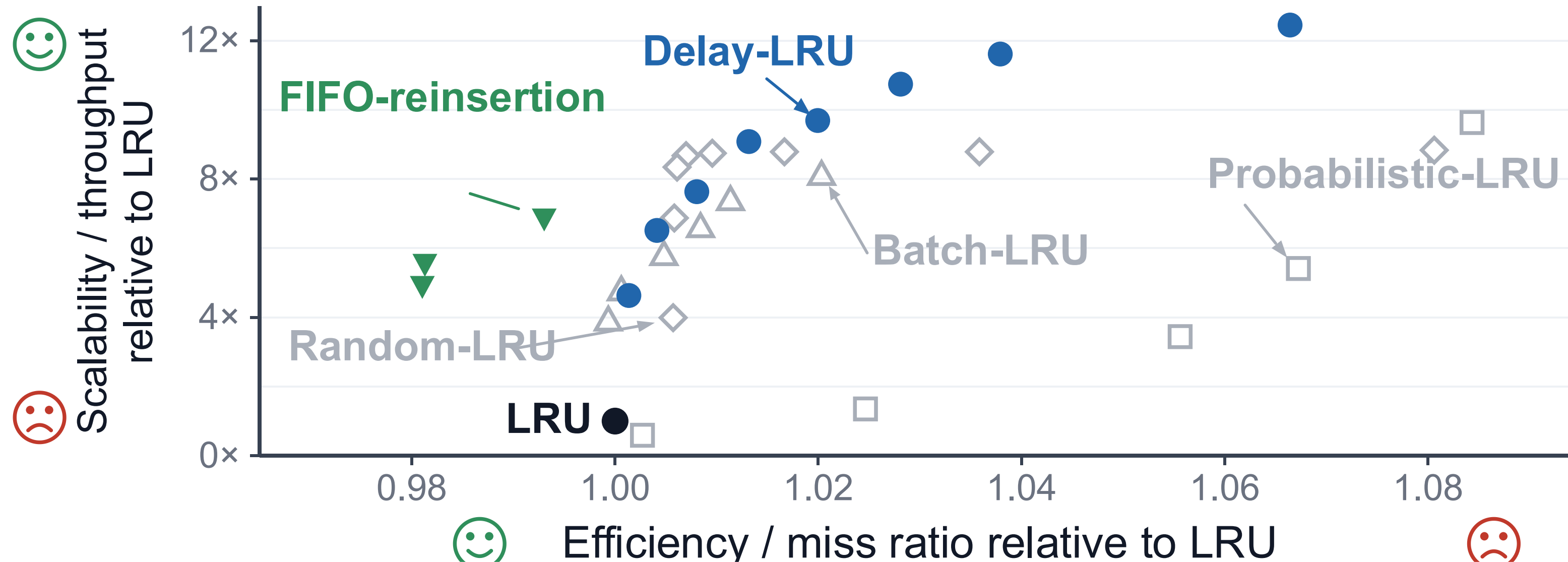


Each technique has its own performance trade-off



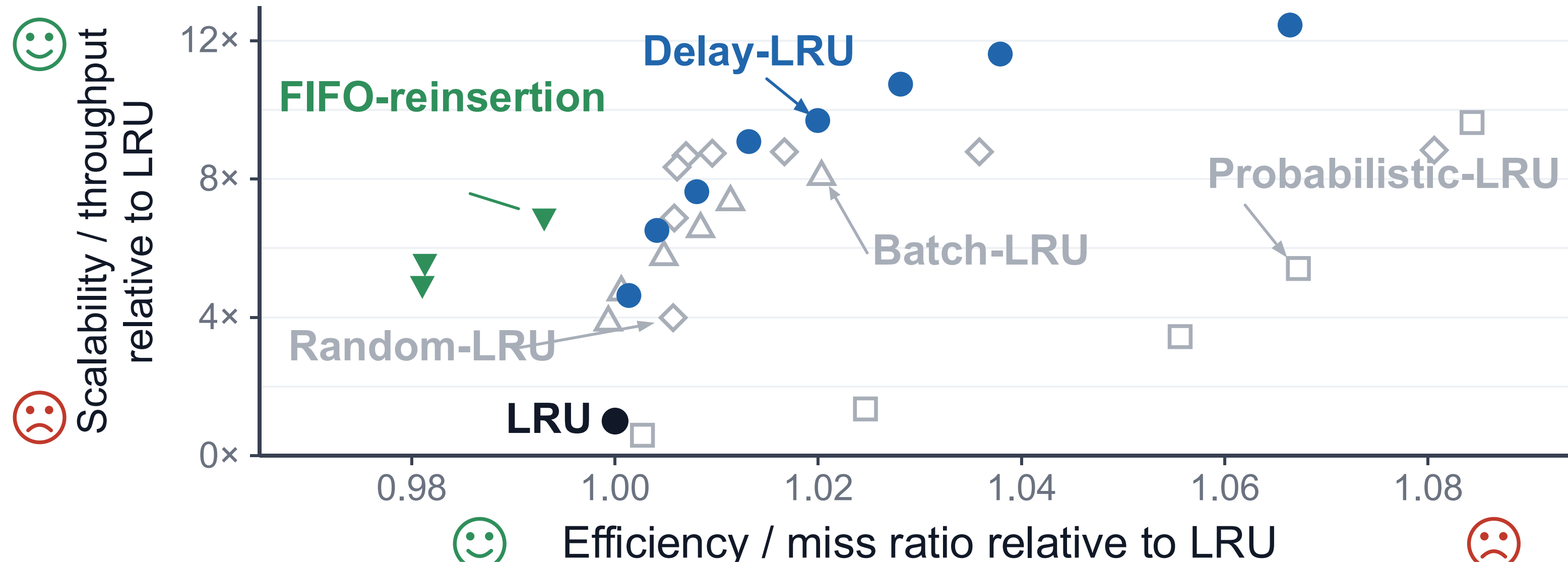
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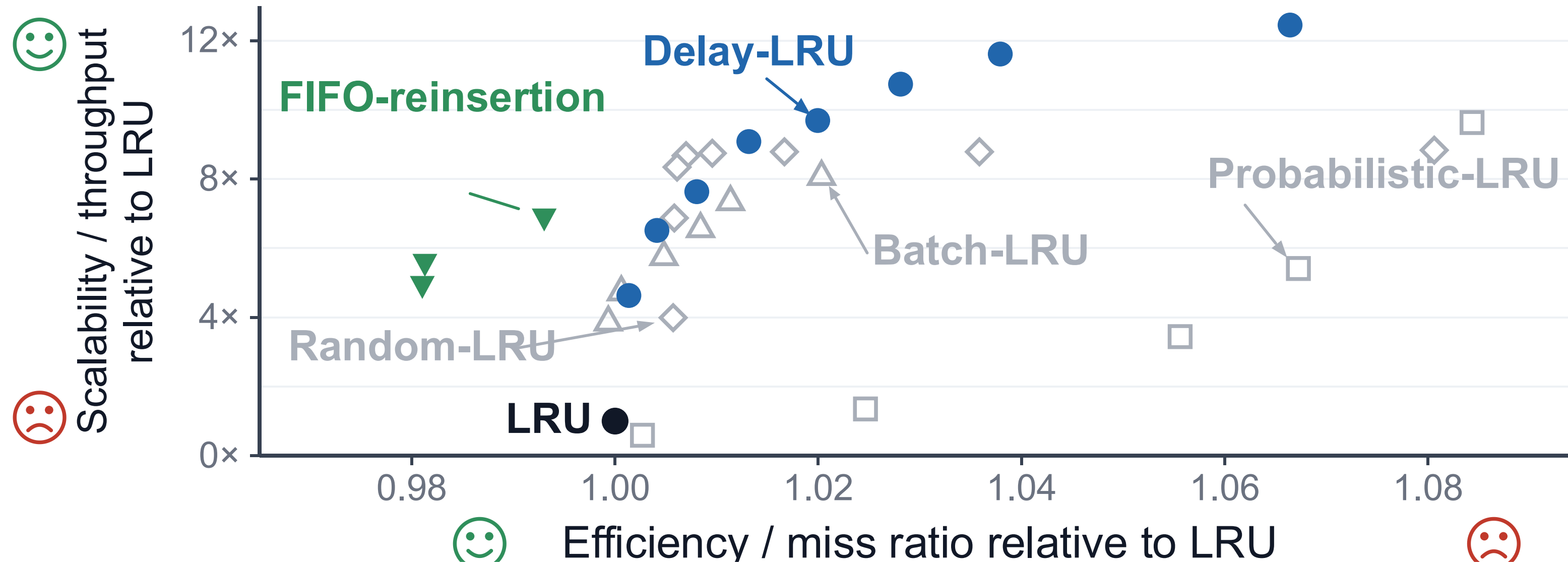
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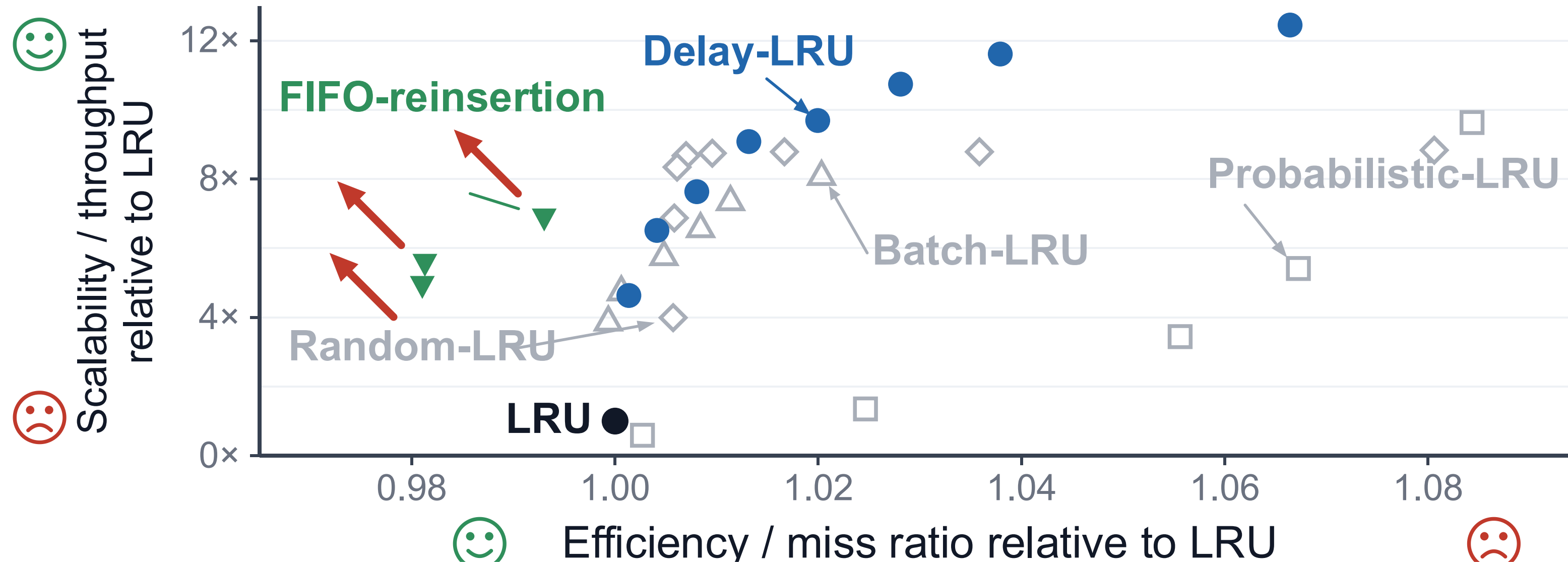
Can we reduce FIFO-reinsertion's promotions without losing its low miss ratio?



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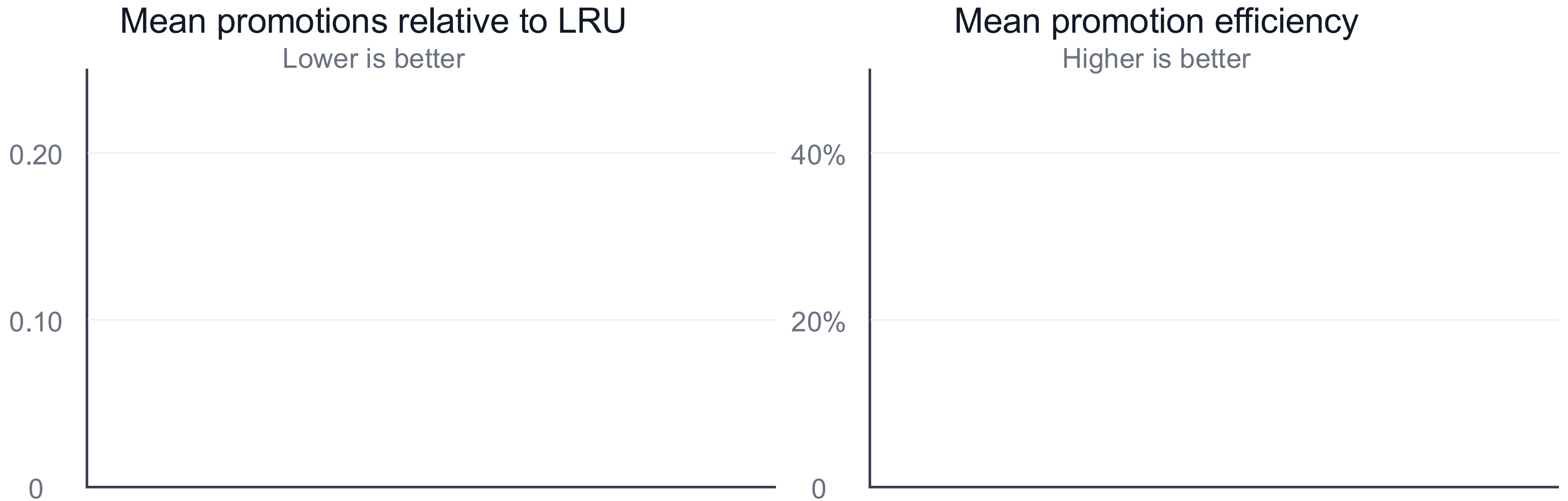
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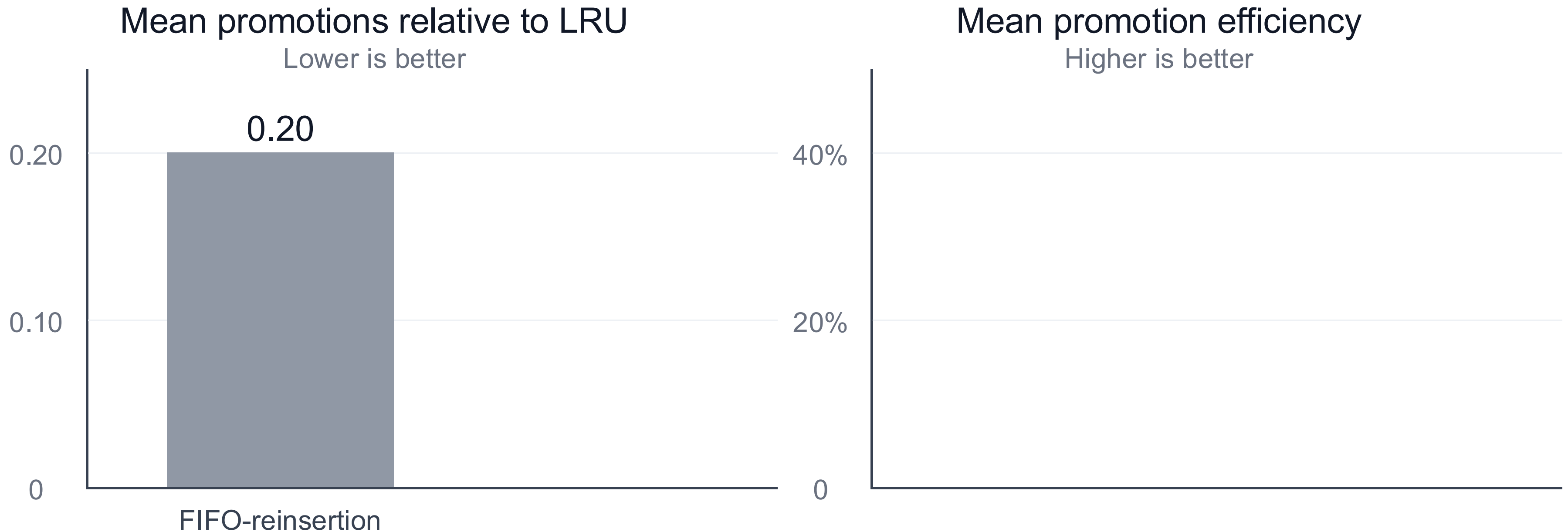
D-FR doubles promotion efficiency

- Delayed FIFO-reinsertion (D-FR) adds a delay window to FIFO-reinsertion.
 - A hit inside the window does not set the marker.



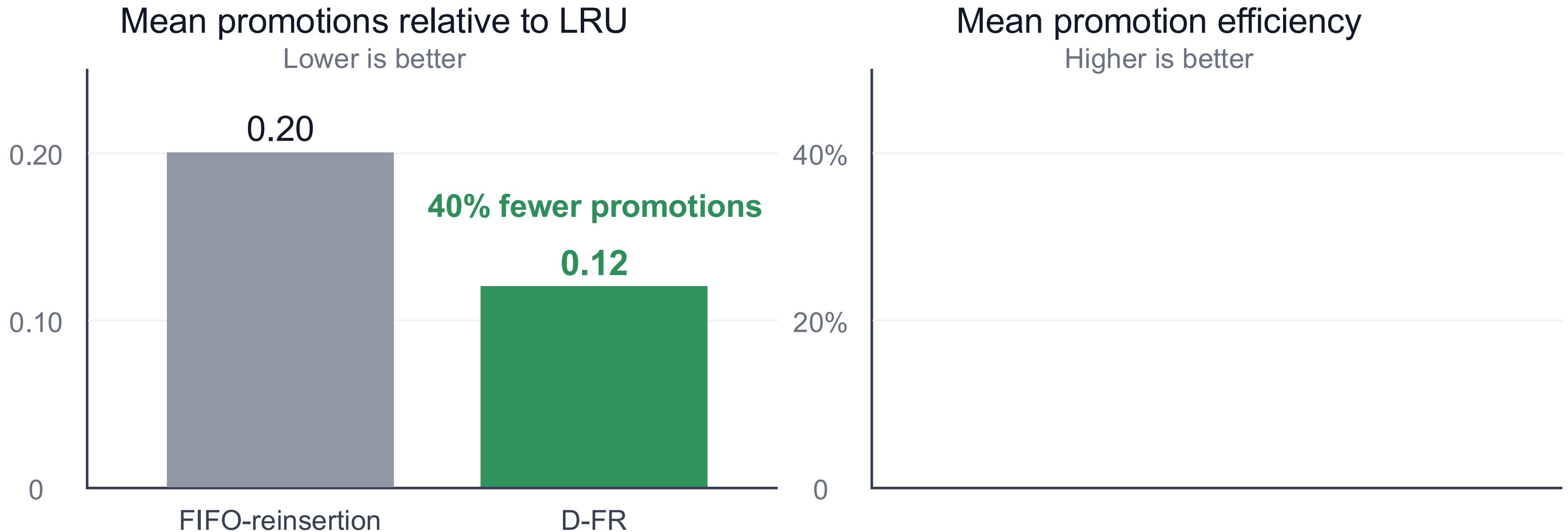
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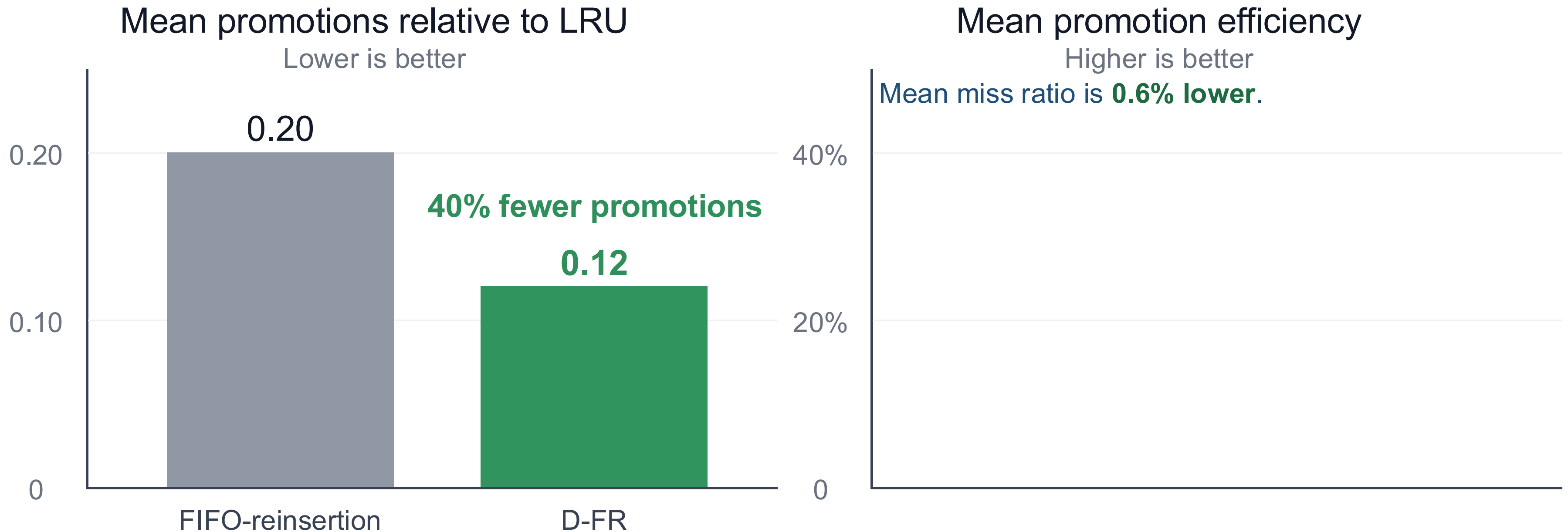


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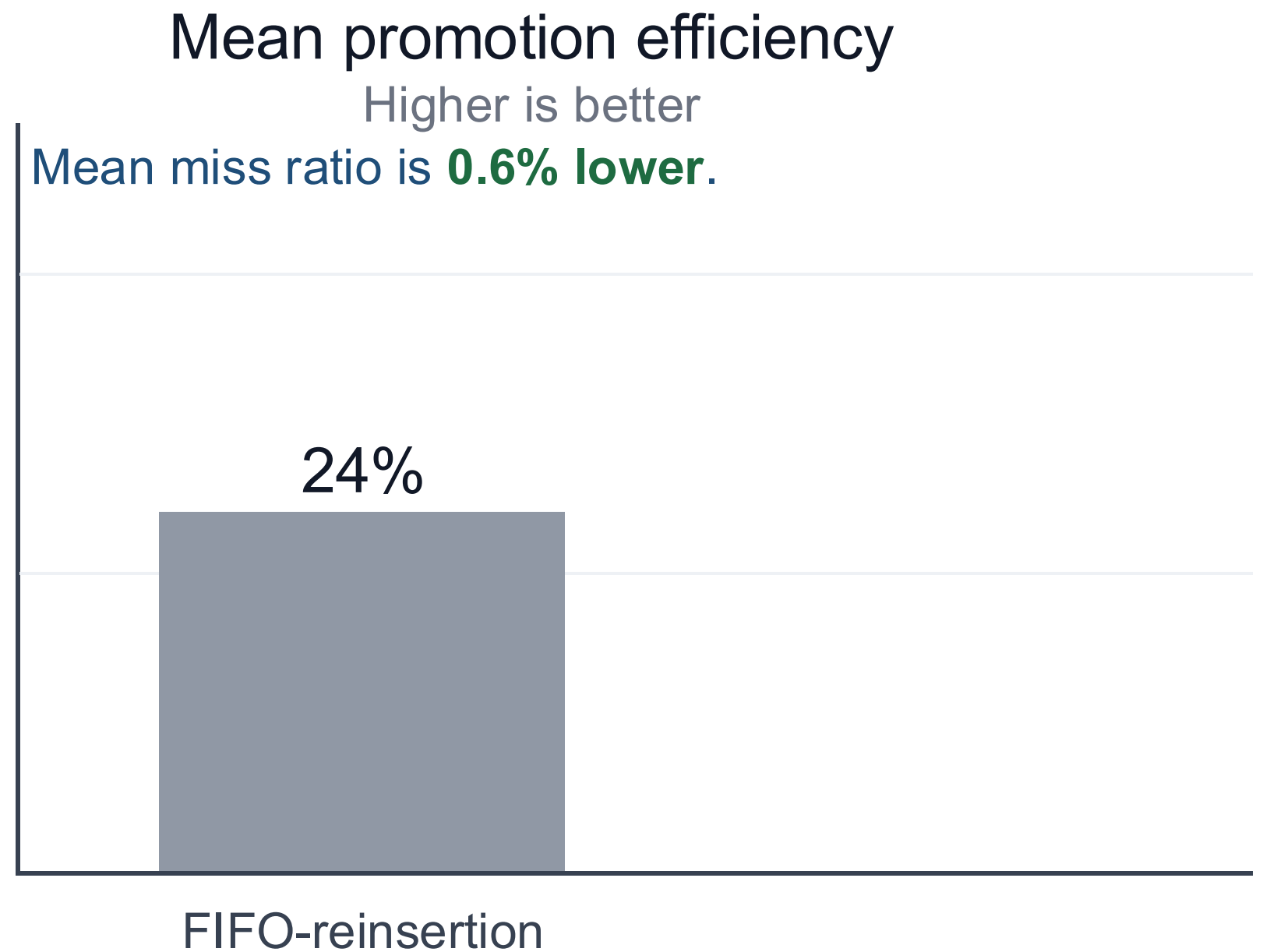
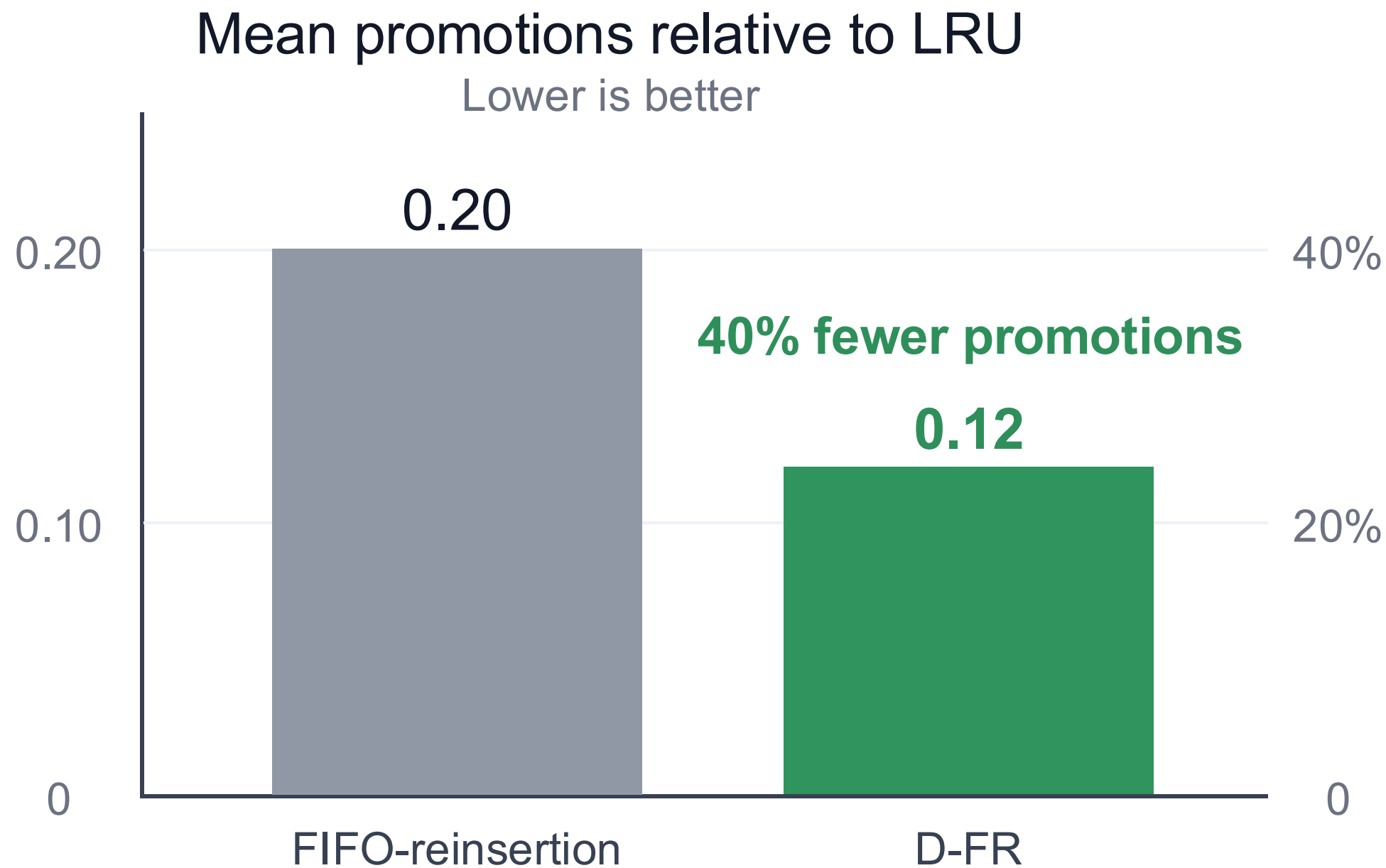


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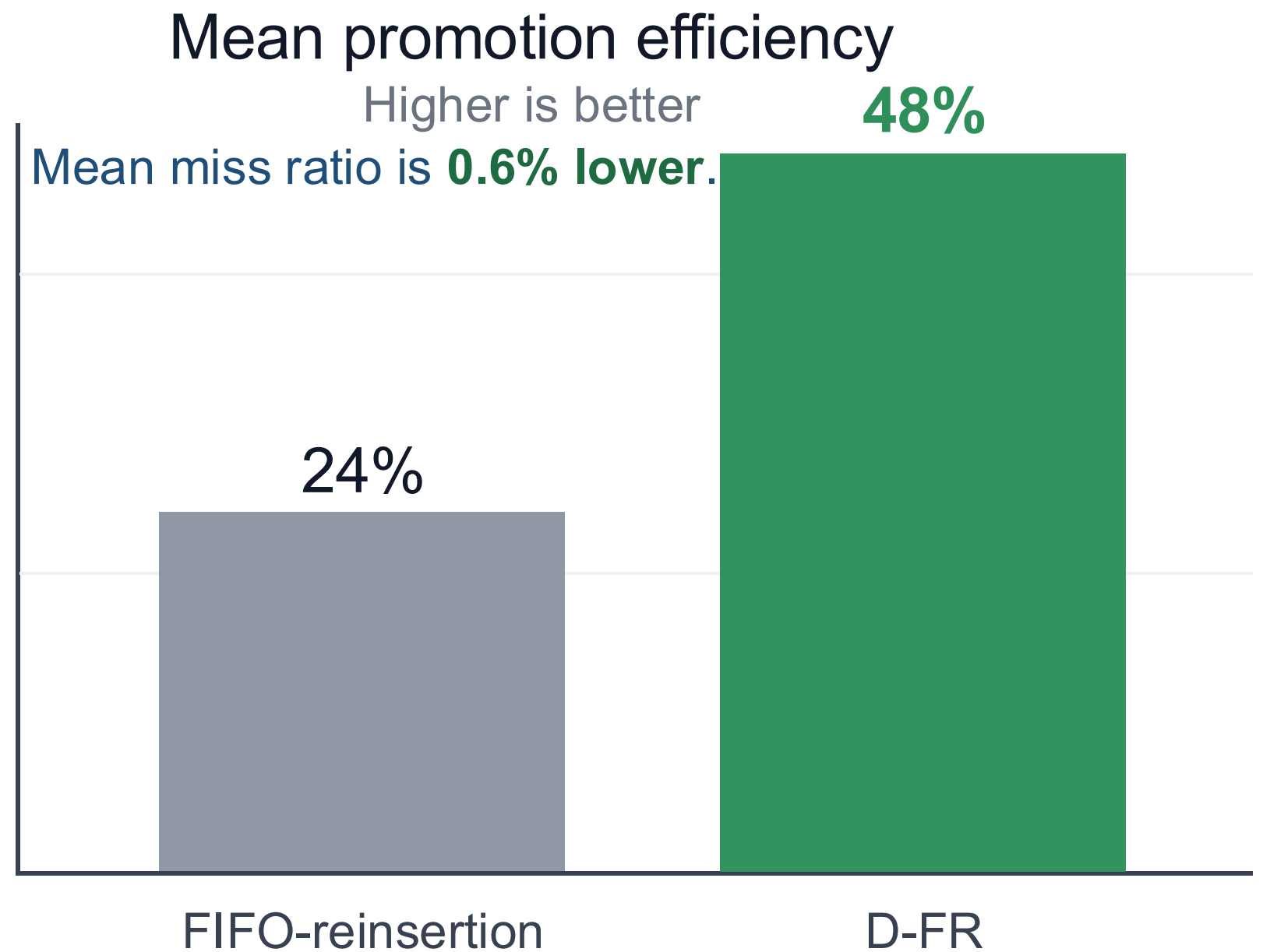
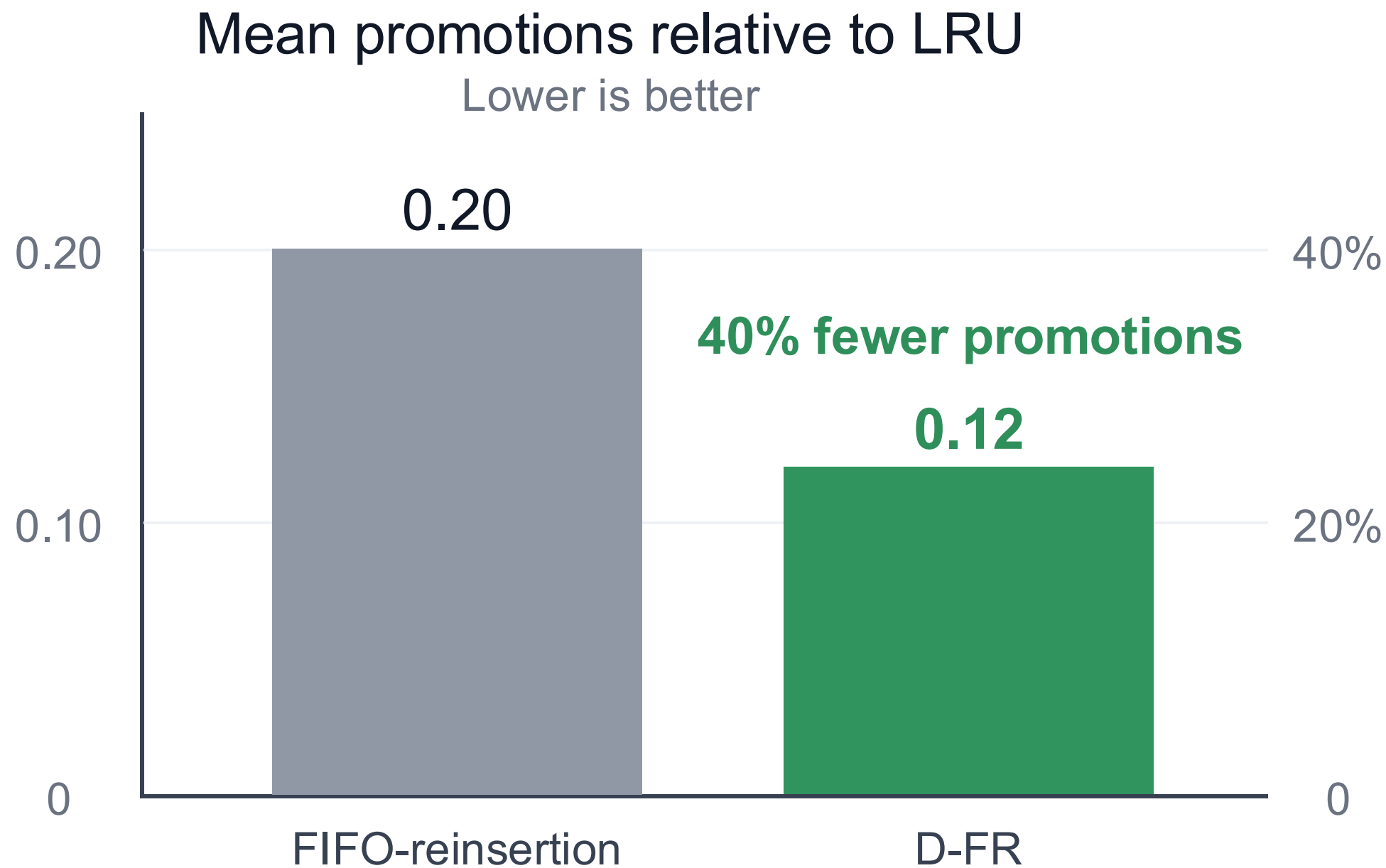


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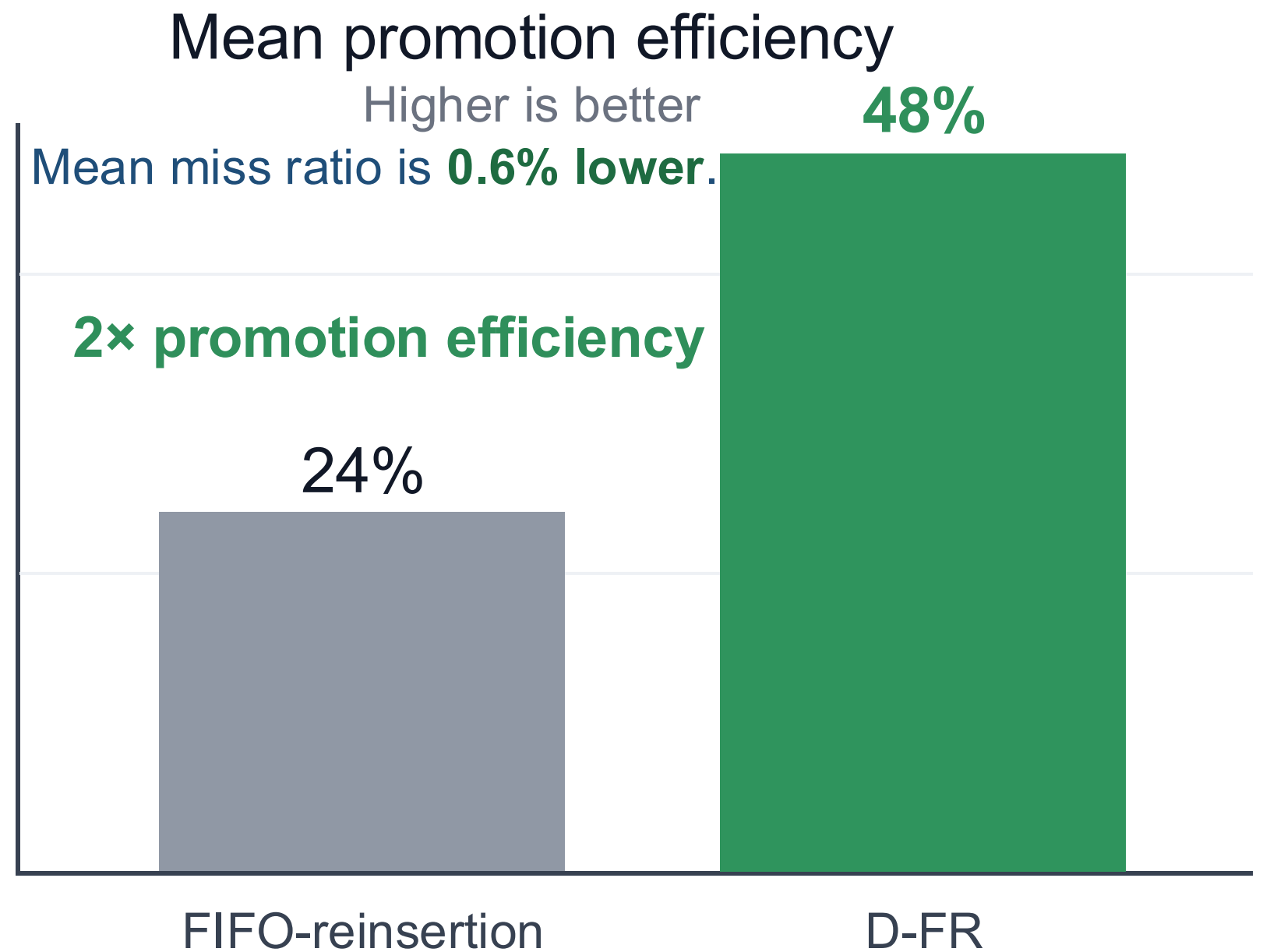
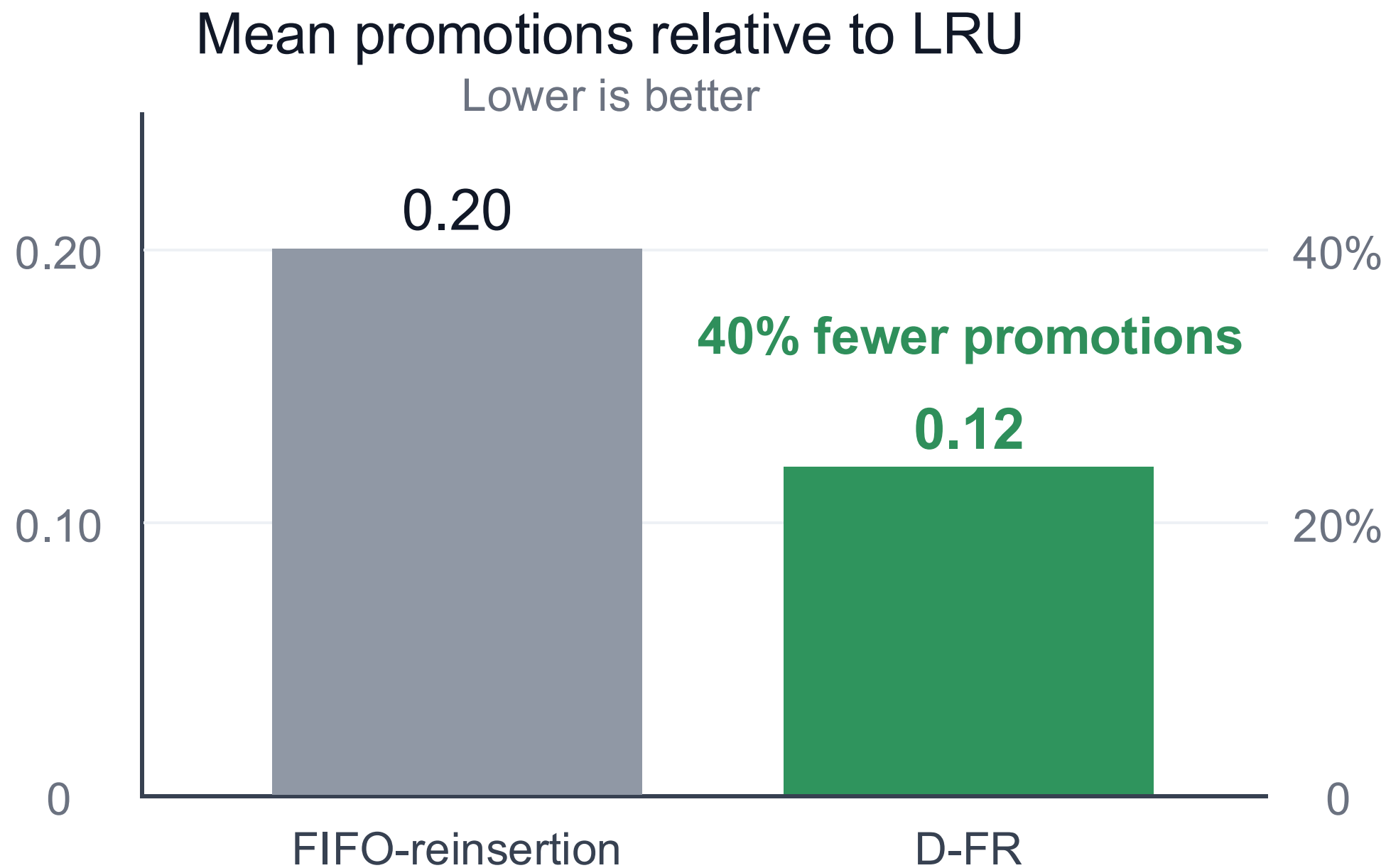


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More in the paper

The paper also studies another design, other cache policies, and the remaining headroom.

- **Age-Guided Eviction (AGE)**

- Skip reinsertion when an object has not been accessed recently.

- **Beyond LRU**

- Evaluate lazy promotion with ARC and 2Q.

- **Oracle analysis**

- Estimate how many more promotions could be skipped.

The five deployed techniques behave very differently

- We present the first comprehensive evaluation of five deployed techniques.

Ziyue Qiu: ziyueqiu@cs.cmu.edu · ziyueqiu.github.io

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CacheMon: cachemon.org



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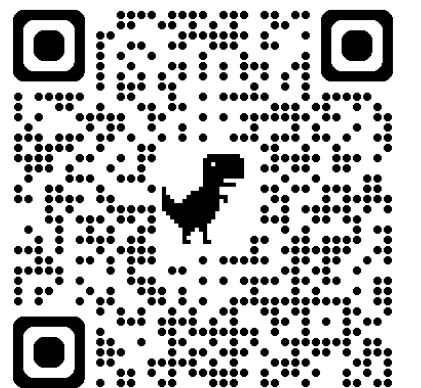
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 - vs. FIFO-reinsertion: **40%** fewer promotions, **2×** promotion efficiency, **0.6%** lower mean miss ratio.

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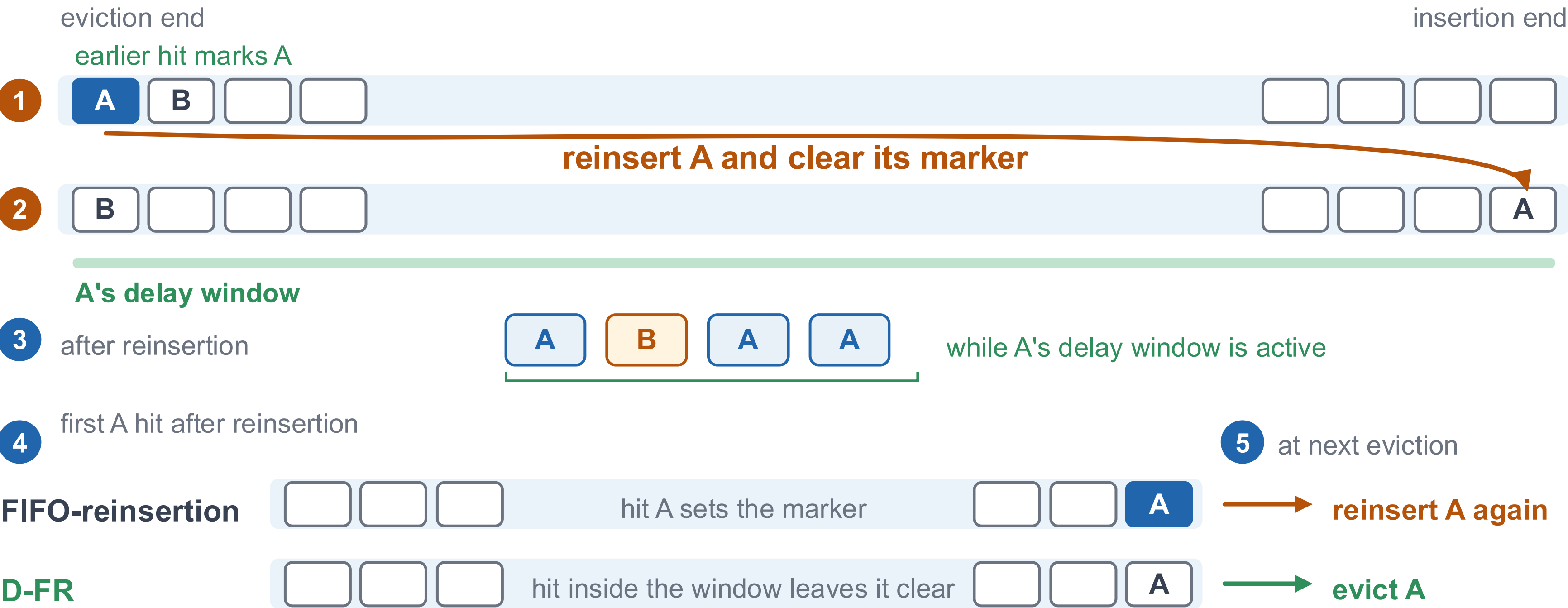


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D-FR keeps the marker clear in the delay window

Backup

- FIFO-reinsertion sets the marker on every hit.
 - D-FR leaves it clear when the object is still inside its delay window.



Plain FIFO-reinsertion may reinsert A twice when nearby hits cross the reinsertion boundary.